

| Table of Contents |                      |
|-------------------|----------------------|
| 2                 | NOTES, BLOCK DIAGRAM |
| 3                 | LOAD BOARD CIRCUIT   |

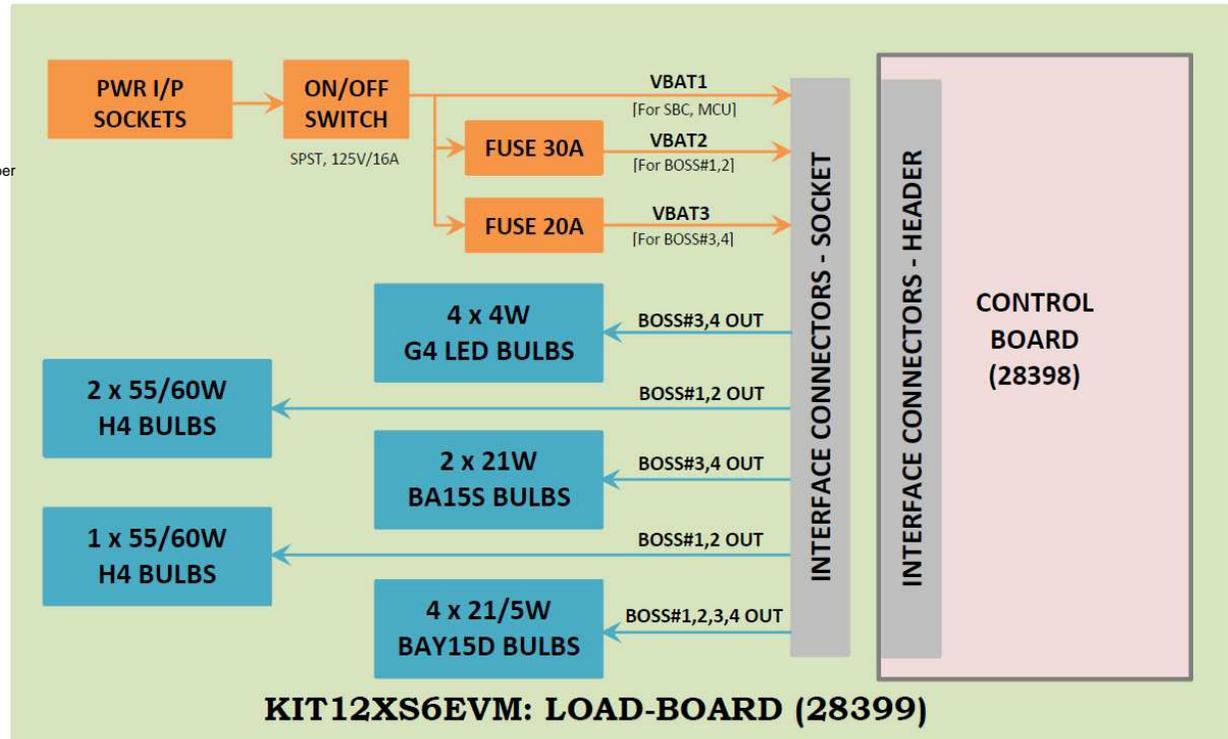
| Revisions |   |           |          |
|-----------|---|-----------|----------|
| Rev       | Description   | Date      | Approved |
| X1        | Initial Design<br>J3, J4, J5, J9 - Pin Mapping updated<br>A070 Release  | 08-May-14 | Li Jing  |
| A         | F1, F2 changed to SUBASSEMBLY Part<br>BOSS1, 2, 3, 4: OUT1 & OUT2 Swapped<br>Ref, Des. Re-sequencing done<br>A085 Release | 22-May-14 | Li Jing  |

# KIT12XS6EVM LOAD-BOARD

|  |           |  |          |
|--|-----------|--|----------|
|   |           | <b>Automotive Product Group</b><br>6501 William Cannon Drive West<br>Austin, TX 78735-8598 |          |
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| Designer:<br>P. Balasubramanian  |           | Drawing Title:<br><b>KIT12XS6EVM_LOAD-BOARD</b>  |          |
| Drawn by:<br>P. Balasubramanian  |           | Page Title:<br><b>TITLE PAGE</b>   |          |
| Approved:<br>Li Jing   | Size<br>C | Document Number<br>SCH-28399 PDF: SPF-28399  | Rev<br>A |
| Date: Friday, May 23, 2014   |           | Sheet 1 of 3   |          |

**NOTES:**

- Unless Otherwise Specified:  
 All resistors are in ohms, 5%, 1/8 Watt  
 All capacitors are in uF, 20%, 50V  
 All voltages are DC  
 All polarized capacitors are aluminum electrolytic
- Interrupted lines coded with the same letter or letter combinations are electrically connected.
- Device type number is for reference only. The number varies with the manufacturer.
- Special signal usage:  
 \_B Denotes - Active-Low Signal  
 <> or [] Denotes - Vectored Signals
- Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

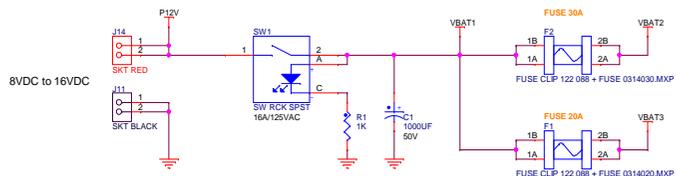


**KIT12XS6EVM: LOAD-BOARD (28399)**



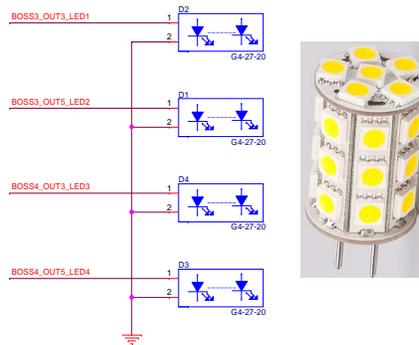
|                            |  |        |            |
|----------------------------|--|--------|------------|
|                            |  |        |            |
| ICAP Classification:       | FCP: _____                               | FIG: X | PUB: _____ |
| Drawing Title:             | <b>KIT12XS6EVM_LOAD-BOARD</b>            |        |            |
| Page Title:                | <b>NOTES/ BLOCK DIAGRAM</b>              |        |            |
| Size C                     | Document Number SCH-28399 PDF: SPP-28399 | Rev A  |            |
| Date: Friday, May 23, 2014 | Sheet 2                                  | of 3   |            |

**POWER SECTION: ON/OFF, PROTECTION**

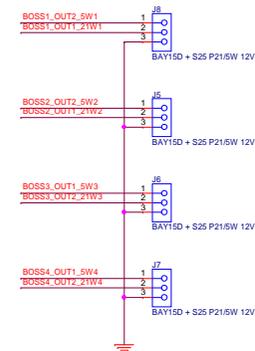


**NOTE: PCB LAYOUT**  
F1, F2 TO BE PLACED IN BOTTOM/SOLDER SIDE

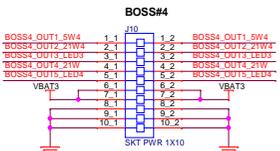
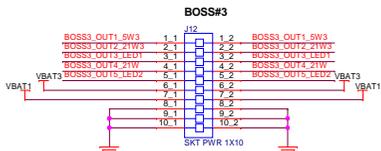
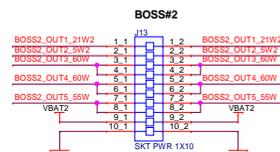
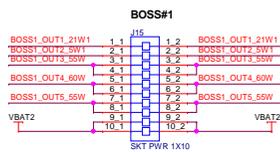
**G4 TOWER LEDs: 4 x 4W/12V**



**INDICATOR LAMPS: 4 x 21/5W,12V BAY15D**

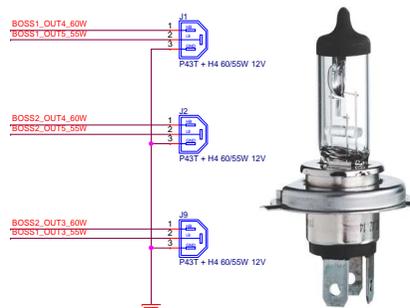


**CONNECTOR SOCKETS: INTERFACE TO CONTROL BOARD**



**NOTE: PIN CONFIGURATION**  
SOCKET PINOUT MAPPING AS PER CONTROL BOARD DESIGN (SPF-28398)

**HEAD LAMPS: 3 X 55/60W, 12V, H4 P43T**



**NOTE: H4 SOCKET**  
PIN1 - HIGH BEAM/60W  
PIN2 - LOW BEAM/55W  
PIN3 - GROUND

**INDICATOR LAMPS: 2 x 21W,12V ,BA15S**

