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Revisions & Change Log			
Rev	Description	Date	Approved
X1	DUT Chip updated	7th May 14	
X2	Banana Socket part number updated	16th May 14	
A	Title Page updated A085 release	19th May 14	

BOSS EMC Daughter Board

		Analog Sensor Product Group 6501 William Cannon Drive West Austin, TX 78735-8598	
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ICAP Classification:		FOP:	FIUQ: PUB: X
Designer: Franklin John Paul	Drawing Title: BOSS EMC Daughter Board		
Drawn by: Franklin John Paul	Page Title: TITLE PAGE		
Approved: Mazeaud Thomas	Size C	Document Number SCH-28395 PDF: SPF-28395	Rev A
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- 54321
- D
- C
- B
- A
- 54321
1. Unless Otherwise Specified:
All resistors are in ohms, 5%, 1/8 Watt
All capacitors are in uF, 20%, 50V
All voltages are DC
All polarized capacitors are aluminum electrolytic
2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.
4. Special signal usage:
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



ICAP Classification: FCP: FIUC: X PUB: X

Drawing Title: BOSS EMC Daughter Board

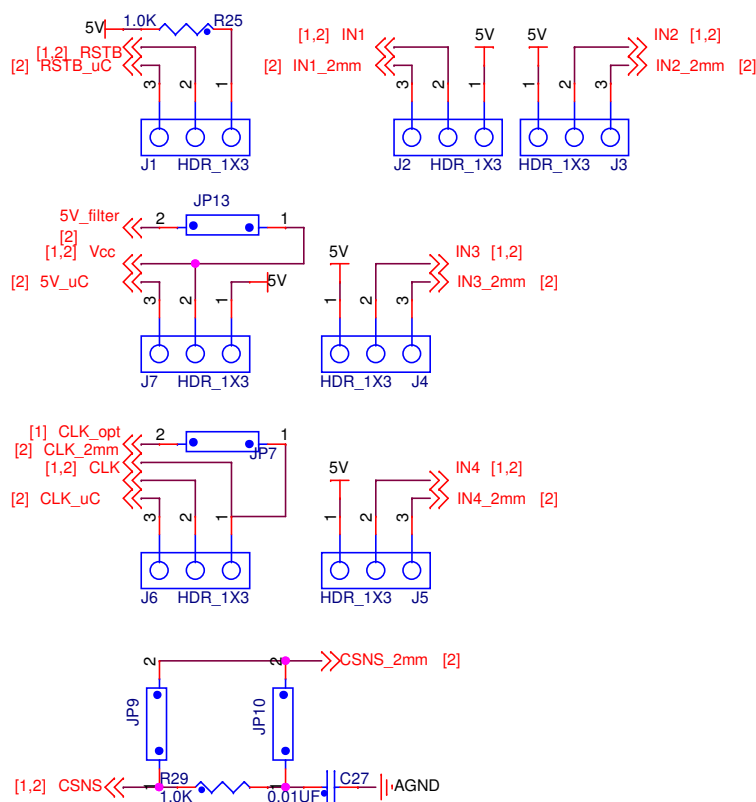
Page Title: Notes

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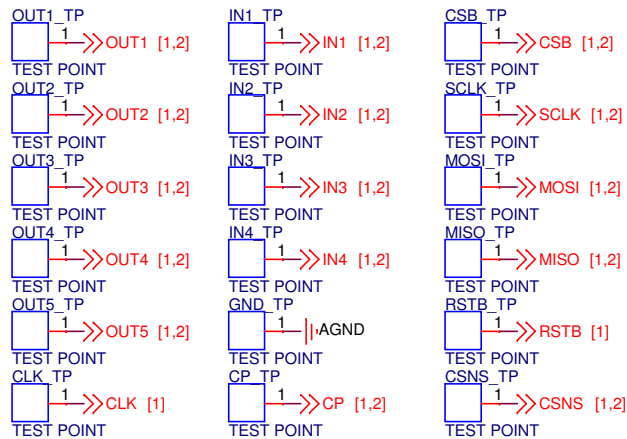
1

Jumpers

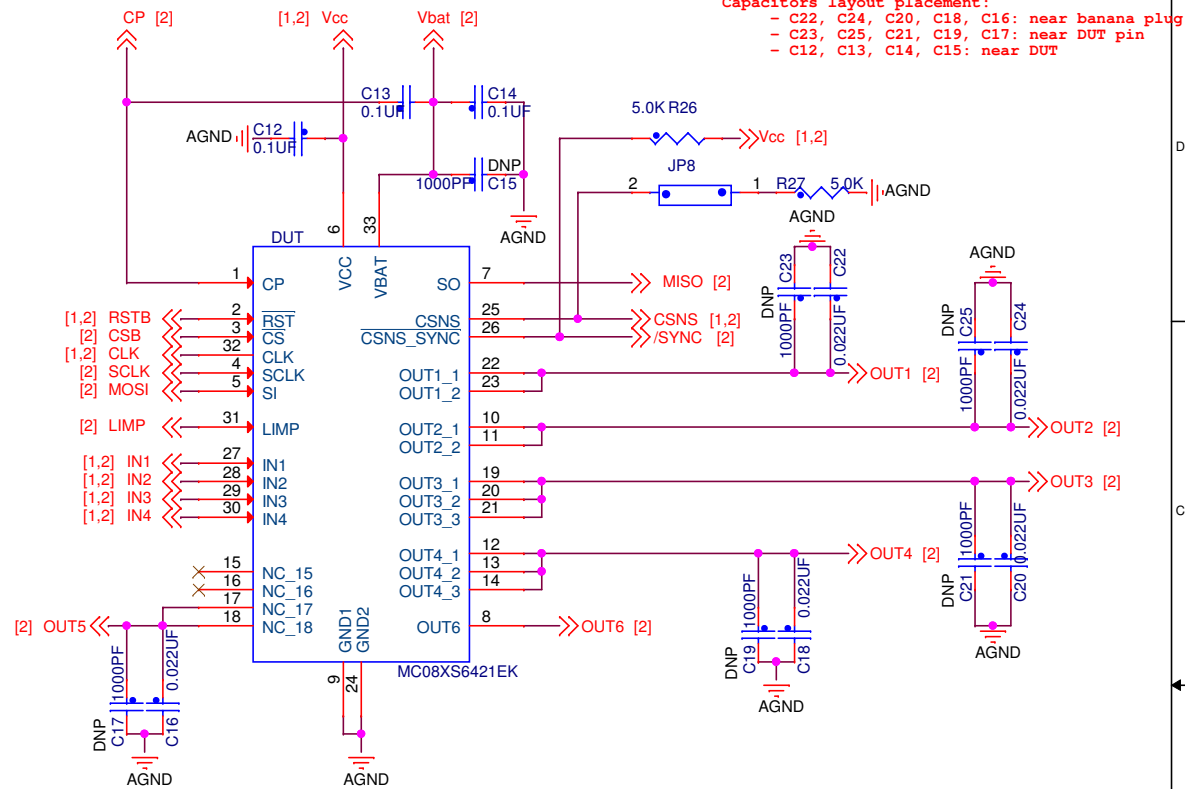


Test Points

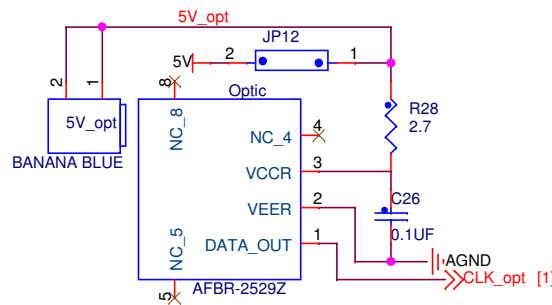
Route OUTx_TP as sense



Device Under Test



CLK optical interface



ICAP Classification: FCP:___ FIUO: X PUBI:___

Drawing Title:

BOSS EMC Daughter Board

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