Point of Sales (POS) Reader Solution - Quick Start Guide

Rev. 1.3 — 27 May 2019 406512

User manual COMPANY PUBLIC

#### Document information

Doodinont informatio	
Info	Content
Keywords	Point of Sales (POS) Reader Solution, K81, PN5180, TDA8035, CLRC663
Abstract	This document intends to describe all steps that should be applied in order to start with the POS Reader Solution kit



**Revision history** 

Rev	Date	Description
1.3	20190527	TWR-POS-CLRC663 HW added
1.2	20170207	PN5180 Firmware update procedure added
1.1	20161116	Update for CES release
1.0	20161110	First release

# **Contact information**

For more information, please visit: <u>http://www.nxp.com</u>

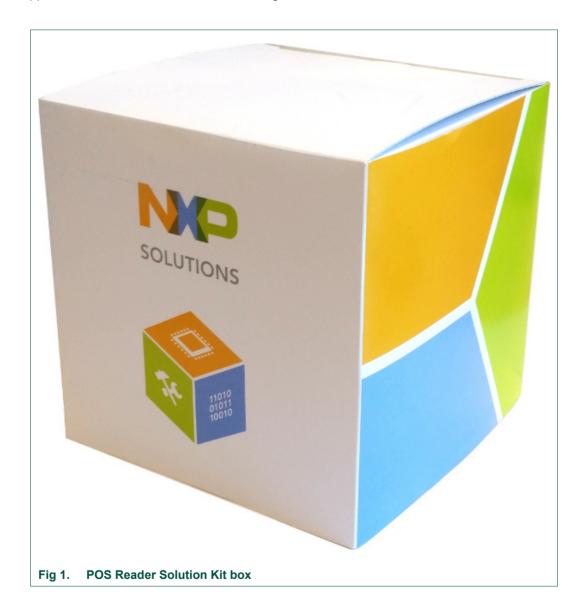
UM11036

© NXP B.V. 2019. All rights reserved.

User manual COMPANY PUBLIC

# 1. Introduction

The K81 point-of-sale (POS) Card Reader Solution is a collection of hardware, software enablement, middleware and specialized application-specific software for the point-of-sale market. This document describes how to start with the kit: use it for payment application demonstration and start working with the dedicated software.



All details about the solution can be found on the NXP webpage:

www.nxp.com/products/reference-designs/point-of-sale-pos-reader-solution:SLN-POS-RDR

UM11036

Point of Sales (POS) Reader Solution - Quick Start Guide

# 2. POS Reader Solution kit

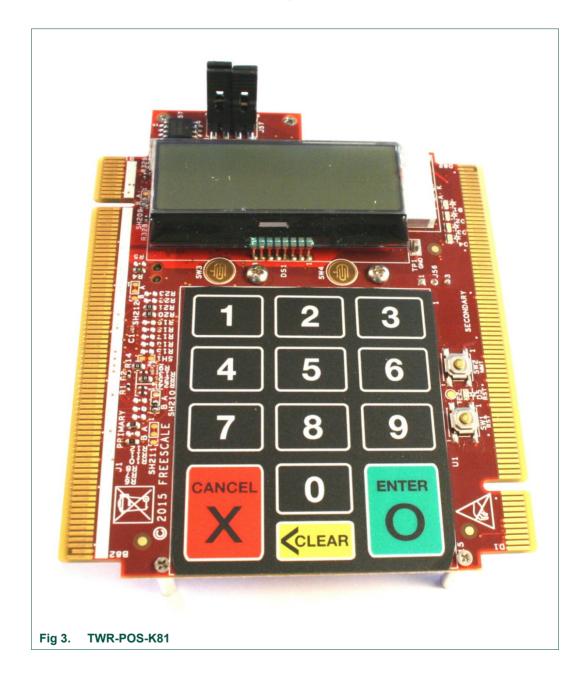
# 2.1 Kit Content



The individual components of the POS Reader Solution Kit are described in the below subsections.

## 2.1.1 TWR-POS-K81

This is the core of the SLN-POS-RDR system. This board embeds the K81 microcontroller, external flash, an LCD display and the secure pin pad.



### 2.1.2 TWR-POS-PN5180

### 2.1.2.1 Presentation

This is the card interface board. It allows reading Contact and Contactless payment cards. This board embeds NXP's PN5180 (contactless frontend) and TDA8035 (contact frontend)



#### 2.1.2.2 Jumper settings

The jumpers on the TWR-POS-PN5180 are used to connect this board to different CPU modules. The setting for these jumpers, to be used in this kit, is the one seen in Fig 4. Below table gives the position of each jumper to be used in this RDR-POS-SLN:

Jumper name	Setting	Jumper name	Setting
J1	1-2	J2	1-2
J3	1-2	J4	1-2
J5	1-2	J6	4-6
J7	4-6	J8	4-6
J9	Open	J10	Open
J11	1-2	J12 1-3-5	1-3
J12 2-4-6	2-4	J16 1-2	Open
J16 3-4	Open	J19	2-3
J20	1-2	J21	Open
J22	1-2	J23	1-2

#### Table 1. TWR-POS-PN5180 Jumper setting for RDR-POS-SLN

### 2.1.3 TWR-POS-CLRC663

### 2.1.3.1 Presentation

This is the card interface board. It allows reading Contact and Contactless payment cards. This board embeds NXP's CLRC663 (contactless frontend) and TDA8035 (contact frontend)



#### 2.1.3.2 Jumper settings

The jumpers on the TWR-POS-CLRC663 are used to connect this board to different CPU modules. The setting for these jumpers, to be used in this kit, is the one seen in Fig 4. Below table gives the position of each jumper to be used in this RDR-POS-SLN:

position of each jumper to be used in this RDR-POS-SLN:

Jumper name	Setting	Jumper name	Setting
J1	1-2	J2	1-2
J3	1-2	J4	1-2
J5	1-2	J6	4-6
J7	4-6	J8	4-6
J9	Open	J10	Open
J11	1-2	J12 1-3-5	1-3
J12 2-4-6	2-4	J16 1-2	Open
J16 3-4	Open	J19	2-3
J20	1-2	J21	Open
J22	1-2	J23	1-2

 Table 2.
 TWR-POS-CLRC663 Jumper setting for RDR-POS-SLN

# 2.1.4 TWR-LCD

This is an LCD module board. This LCD touchscreen is used to interact with the user: display information and get inputs from the user.



## 2.1.5 TWR-Elev

These are the connection boards. They are needed to connect all feature boards together



### 2.1.6 Accessories

#### 2.1.6.1 Cables

Two USB cables are provided: One USB Mini to provide power to the system, and one USB Micro to interface with a computer

### 2.1.6.2 Battery

A CR2032 Button Cell battery is provided. The battery has to be inserted in the battery slot on the TWR-POS-K81 board

### 2.1.6.3 Sample Card

One sample card is included. It is a demo Payment card with dual interface (Contact + Contactless). The card embeds a secure processor with JCOP OS, running a payment application.

This payment application can be accessed through Contact or Contactless interface.



### 2.2 Setup the kit

The kit is delivered already assembled, but in case it is received disassembled or is disassembled during operation, the next chapters describe how to assemble it.

### 2.2.1 TWR-Elev Primary and Secondary boards

It is important for the next steps to differentiate the Primary and Secondary TWR-Elev boards. Depending on the version, the name of the board can be written:

"Secondary board" or "Primary board" on the inner side (i.e the side with the 4 female PCI connectors).

If the name is not printed on the board, the Primary can be found by its marking "**A** side expansion port" and "**B** side expansion port", while the Secondary board embeds "**C** side expansion port" and "**D** side expansion port"

### 2.2.2 Assemble the Hardware

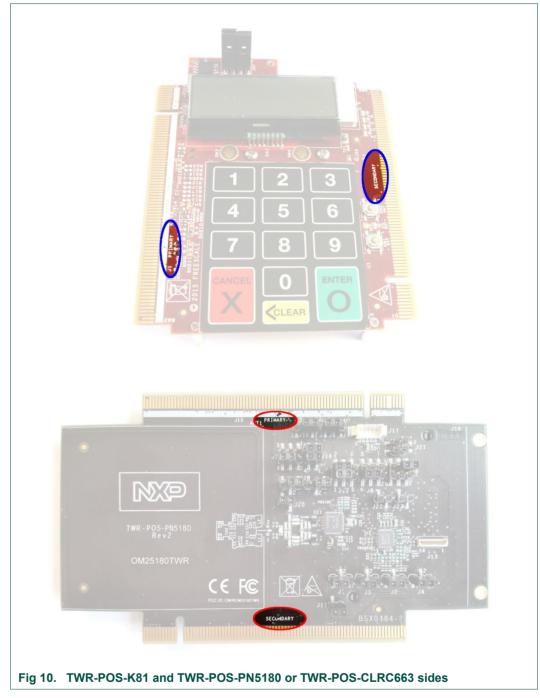


The TWR-LCD has to be connected on the outside of TWR-Elev Primary board.

TWR-POS-K81 and TWR-POS-PN5180 or TWR-POS-CLRC663 have to be connected in the TWR-Elev PCI slots.

The primary and secondary connectors of each board have to be connected respectively on the primary and secondary TWR-Elev boards. The side can be recognized by its marking on each board as shown in Fig 10.

**UM11036** 



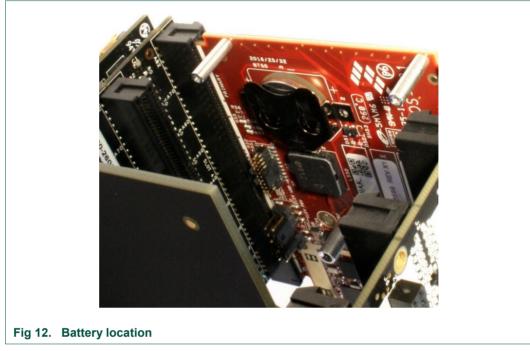
The most convenient way is to have the K81 board on the top connector, and the PN5180 or CLRC663 board on the bottom connector:



User manual

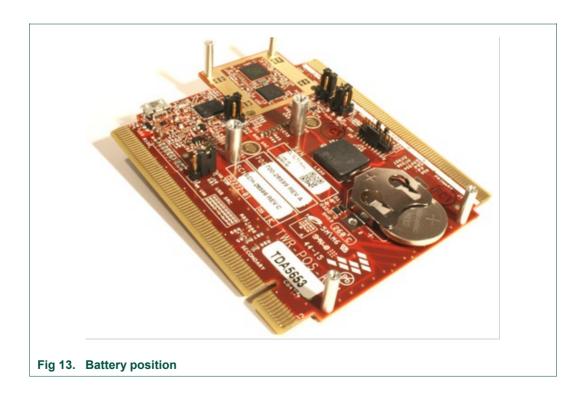
**COMPANY PUBLIC** 

**UM11036** 



The battery has to be inserted in the battery slot, underneath the TWR-POS-K81 PCB:

To insert the battery in the right way, the + symbol has to be on top (visible), as seen in next figure:



# 2.3 USB Connections

The Kit presents 3 USB connectors:

- One Mini USB on the TWR-Elev board
- One Mini USB on the TWR-LCD board
- One Micro USB on the TWR-POS-K81 board

See Fig 15 and Fig 16 for each connector's location.

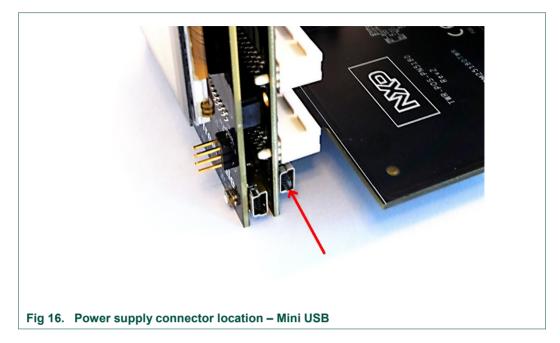
In order to operate the Tower Kit, both the Micro USB, underneath TWR-POS-K81 board, and the Mini USB on the TWR-ELEV board have to be used.

Micro USB has to be connected to the host computer (running the high-level application) and the TWR-ELEV mini USB has to be connected to a power source (either from a computer USB or from a power socket USB). Both are needed to ensure the Kit will have enough power to operate in all modes.





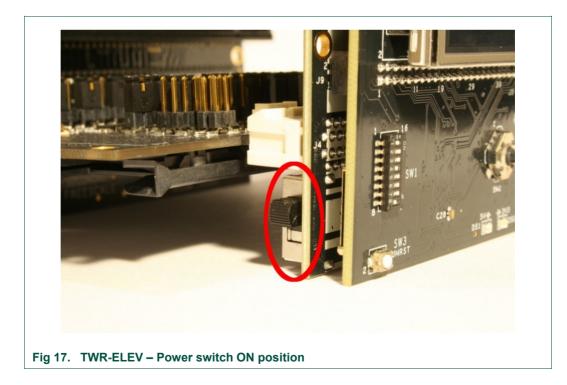
UM11036



## 2.4 TWR-ELEV Power Switch

A switch is mounted on the TWR-ELEV board. This switch is used to turn ON or OFF the power supply from the TWR-ELEV USB connector.

For the SLN-POS-RDR to operate, the switch has to be in the ON position (top) shown in Fig 17, otherwise there will be no power from this TWR-ELEV connector.



# 3. Demonstration

## 3.1 Setup the environment

After the Hardware is assembled and connected, the computer has to be set up in order to operate the payment demonstration: the TWR\_POS\_K81 module embeds a USB to Serial converter, accessed through the mini USB port connected to the computer.

When this USB is connected, the host computer should display an unknown device in the device manager.

The dedicated driver for this device has to be installed so that the computer gets a new COM port that will be used to connect to the POS Reader Solution Kit. This can be done following the below steps:

- Locate the .inf file for this driver: It is delivered with the software package. It is called fsl\_ucwxp.inf and is located in the folder "boards\twrposk81\demo\_apps\payment\_demo\cardtek\_ihs\_app"
- 2. Open the device manager
- 3. Locate the unknown device. This should be a device with a Yellow Bang, named "MCU VIRTUAL COM DEMO". See Fig 18.
- 4. Right click on this device, and choose "Update Driver Software"
- 5. Select "Browse my computer for driver software"
- 6. "Let me pick from a list..."
- 7. "Show all devices" then "Next"
- 8. "Have Disk"
- 9. Click "Browse", navigate to the folder containing the above mentioned .inf file, and select this .inf file.
- 10. Windows should propose a Virtual COM port (see Fig 19). Select it then click "Next"
- 11. Note that Windows may show a warning saying that the driver is not digitally signed. To install it anyway, the computer must be configured to disable driver signature enforcement.

If the computer is not correctly configured, it can be done following Microsoft help.

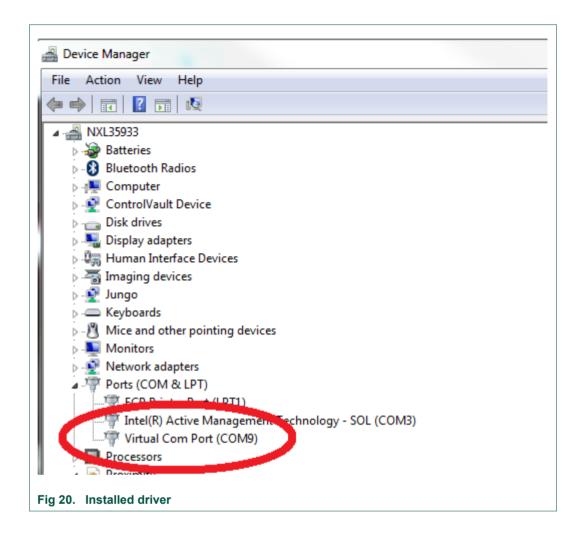
E.g. <u>https://msdn.microsoft.com/en-us/windows/hardware/drivers/install/installing-an-unsigned-driver-during-development-and-test</u>

Note that the above link may not give the exact process depending on the computer OS version.

- 12. Windows may then show some messages asking to confirm that you want to install this driver. Accept by selecting "Install this driver Software anyway"
- 13. Windows will install the driver
- 14. When the driver is installed, it may happen that Windows shows a message informing that the driver cannot start. In such a case, unplug then plug back the USB connection. This should restart the driver, so that it is seen as running (no error) in the device manager (see Fig 20).

<u>File Action View H</u> elp	
🔄 🧼 📰 🛛 🛐 🛐 🗔 🛝	
🦻 👰 Jungo	*
⊳ ·	
Mice and other pointing devices	
⊳ - 🖳 Monitors	
🖟 👳 Network adapters	E
Other devices	
🔄 🐚 MCU VIRTUAL COM DEMO	
> - III Portable Devices	
Ports (COM & LPT)	
Processors	
⊳ - 🙀 Proximity	-

Select the device driver you want to install for this hardware.
Select the manufacturer and model of your hardware device and then click Next. If you have disk that contains the driver you want to install, click Have Disk.
Model Virtual Com Port
This driver is not digitally signed! <u>Have Disk</u> <u>Tell me why driver signing is important</u>
Next Canc



UM11036

User manual

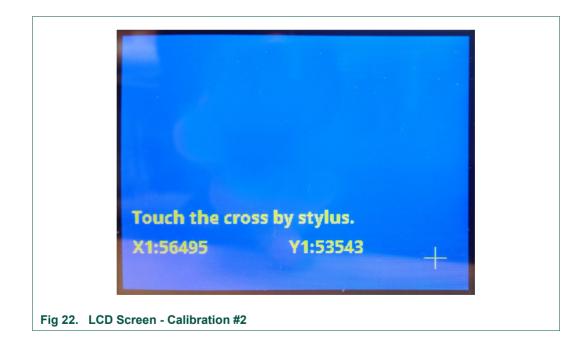
**COMPANY PUBLIC** 

# 3.2 First power-up of the kit

At first power-up, the POS-RDR-SLN kit will ask for a calibration of the side LCD screen. This calibration requires the user to touch the screen at two specific locations.

To do so, touch the screen on the displayed crosses when prompted See Fig 21 and Fig 22.





Right after this operation, the LCD will display the demo screen. See next chapter.

UM11036

# 3.3 Payment Demo application

This application demonstrates a full payment application using a sample payment card, and an issuer payment application, running on the computer. This tool is called "Issuer Host Simulator" (IHS).

The following chapters describe the way to use the default demonstration. For more details, refer to [2].

### 3.3.1 Launch the demonstration

To start the demonstration:

- 1. Plug the POS Reader Kit Mini USB (power)
- 2. Plug the Micro USB to the computer (communication). Note the COM port value assigned in the device manager.
- 3. Launch the Issuer Host Simulation (IHS) Tool that can be found in the SW release package, in the folder

K81POSCR\_SW\_Release\boards\twrposk81\demo\_apps\payment\_demo\cardtek\_ih s\_app

The tool is the application named "IHS.exe".

	ISSUER HOST SIMULATI	
ETTINGS	MONITORING	
Issuer Host Configuration	Field 55 Data	Field 55 Response Data
Issuer Master Keys		
AC Key		
MAC Key 1919191919191919191919191919191919		
ENC Key		
Authorization Response Code(Tag 8A)		
Approve     Decline		
Use "Online Pin" and "ARQC" Validation Result		
Online Pin Configuration		
Pin Key 766572796e69636570617373776f72643132333435363738		
Default Online Pin		
1234		
Scripts(Tag 72)		
No Script		
PIN Unblock		
PIN Change		
Put Data		
Tag Value		
	I	
COM Port Configuration	Visa Issuer Application Data(9F10)	
- CONNECT DISCONNECT	Cumulative Total Transaction Amount	VLP Available Funds   CVR Details
	APDU/SERIAL PORT TRA	CE

4. In the COM Port configuration window, select the COM port value noted at step 2.

- 5. Click on "Connect".
- 6. !! First time only !! The first time the IHS is connected to the tool, it will download some configuration files. Wait for this process to finish, then the demo can be used.
- 7. The TWR-ELEV LCD (color LCD) asks to pick an action: "Config" or "Payment".

SOLUT	FOR A S	E CONNECTIONS SMARTER WORLD	
	Pic	k action	
	Config	Payme	nt
	•	•••	
Fig 24. Demo start	screen		

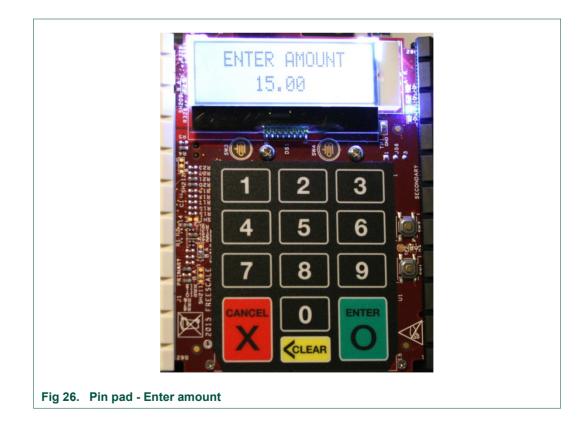
- 8. Select "Payment" by touching the screen
- 9. The application asks to enter a transaction amount. This value has to be entered with the secure pin pad on top of the solution. (e.g. 15.00 can be entered)

User manual

**COMPANY PUBLIC** 

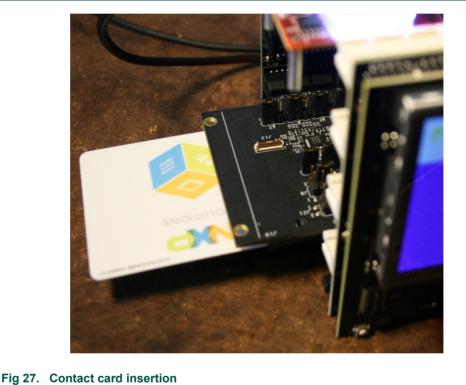


10. The amount is displayed on both LCD screen: on the TWR-POS-K81 2 lines LCD, and on the side color LCD.



UM11036

- 11. Press "Enter" on the secure pin pad to validate the amount.
- 12. The LCD screens ask to insert or tap a card. From this point, Contact or Contactless transactions can be used. For contact transaction, go to step 13, and for contactless operation, go to step 15.
- 13. Contact operation
  - a. Insert the sample card into the Contact Smart Card reader (located under the TWR-POS-PN5180 module or TWR-POS-CLRC663 module). The smart card has to be inserted with contacts and prints on top:



- b. The process continues and you are requested to enter the card PIN. Enter the 4 digits pin code on the TWR-POS-K81 module pin pad, then press Enter. The default pin code is "1234".
- c. If the POS Reader Solution is connected to the IHS, and the IHS is active, the LCD Screen will show "Online Approved Remove Card", and the transaction information will be displayed in the IHS dedicated window.



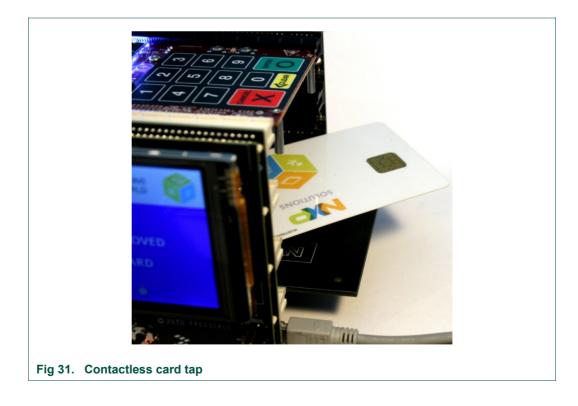
ETTINGS	MONITORING	
Issuer Host Configuration	Field 55 Data	Field 55 Response Data
Issuer Master Keys		
	FIELD 55 TERMINAL DATA :	FIELD 55 RESPONSE DATA : 8A023030910AA18341DBF88B62FC3030
AL Ney	06000000000009F1A020056950508900000005F2A0209789	
MAC Key 181313131313131313131313131313131313131	F370400000009F2701809F360200779F2608B7A1404116935	AUTHORISATION RESPONSE CODE
ENC Key	DEE9F101706010A039400120F03999999999988888888888888 F00D8282023C009A030001019C0100	02 3030
Authorization Response Code(Tag 8A)	TRACK 2	ISSUER AUTHENTICATION DATA
Approve     O     Decline	57 00	91
Use "Online Pin" and "ARQC" Validation Result	00	10 A18341DBF88B62FC3030
Online Pin Configuration	TERMINAL ENTRY CAPABILITY	SCRIPT TEMPLATE 71
Pin Key	DF8147	71
766572796e69636570617373776f72643132333435363738	00	00
Default Online Pin	MSD TRACK 1	
1234	DF8148	SCRIPT TEMPLATE 72 72
Scripts(Tag 72)	00	00
No Script	MSD TRACK 2	
PIN Unblock	DF8149	
PIN Change	00	
Put Data	TERMINAL TRANSACTION OUALIFIER	
Tag Value	9F66	
Tag Value	00 👻	
COM Port Configuration	Visa Issuer Application Data(9F10)	
COM16 - CONNECT DISCONNECT	Cumulative Total Transaction Amount 0000003000   VLP Available Fun	ds 0000100000   CVR 03A42002 Details
	APDU/SERIAL PORT TRACE	
2:27:02] HOST MESSAGE :	009F101706010A03A420020F03000010000000000000000DF3FEBFA9F02060000000	1 5 0 0 5 0 0 5 0 0 0 0 0 0 0 0 0 5 0 5
A9C01009A030001015A08476173900101001095050080000000 2:27:031 HOST REPLY MSG : 6A023030910AA18341DEF886		
SISTING HORI REFLINDE : DAUSSUSUAINAIDSAINDEDDD.	rcauau	

d. If the POS Reader Solution is not connected to the IHS, the LCD will display "Offline Approved – Remove card"

UM11036



- 14. You can now remove the card. The setup is ready for the next operation.
- 15. Contactless operation
  - a. When the POS asks to "Insert or Tap a Card", the sample card can be tapped on the TWR-POS-K81 board antenna:



 b. This operation does not request a pin code to be entered. When the card is detected, the transaction is processed and the POS displays "Offline Approved – Remove Card"



User manual COMPANY PUBLIC

# 3.4 Factory Reset

If the system needs to be set back to its default configuration, it is possible to reset it with the factory settings.

To do so, at start-up screen, choose "Config":

N so		CONNECTIONS		
	Pic	c action		
(	Config	Paymer	it	
	00			
Fig 33. Factory reset	#1			

Then scroll down the configuration screen to display "Factory Reset" Touch "Factory Reset" and then "OK":

POSIC	onfig	
Set-Date-Time Get-Date-Time CVM Settings Config File		
Entory Reset	Cancel	1



The system will reset its data internally. Several screens will be seen:

The process is finished when the screen shows back the "POS Config" menu screen. At this point, the HW has to be reset: unplug the two USB cables, and plug them back.

The system will restart with the screen calibration. See 3.1.

User manual

COMPANY PUBLIC

# 4. POS Reader Solution Software

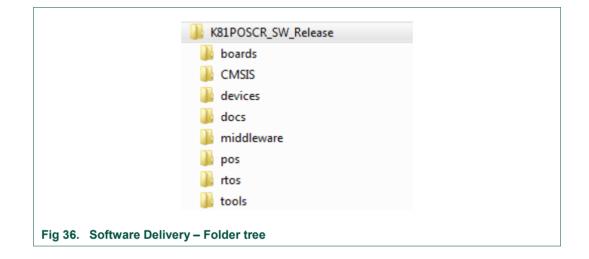
## 4.1 Introduction

The POS Reader Solution kit is delivered with a pre-installed software running the Payment Demo application.

The Software code can also be obtained directly from NXP. Contact your direct NXP representative to get access to this software.

The software is delivered in a package containing source code from NXP and libraries from third companies.

The folder tree from this SW code delivery is as follows:



The "boards" folder contains the demonstration example projects that are prepared for two IDEs (Integrated Development Environment): IAR and KDS.

Other folders contain source code and libraries for all embedded features.

For deeper details about the SW content, refer to [1].

# 4.2 Software project

To open the software project, edit, compile and load the Software into K81 target, 2 IDEs can be used: IAR and KDS.

### 4.2.1 Debug Probe – J-Link

The program download into the K81 requires a J-Link probe to be connected between the computer and the K81 target.

J-Link probe is used for this purpose and can as well be used to debug the program running in the K81 target. This connection requires at a minimum a J-Link Base probe, and a 19-pin Cortex-M Adapter, to connect the probe to the K81 module:

https://www.segger.com/jlink base.html

https://www.segger.com/jlink-adapters-19pin-cortexm.html

Probe drivers should be automatically installed during the installation of the IDE.

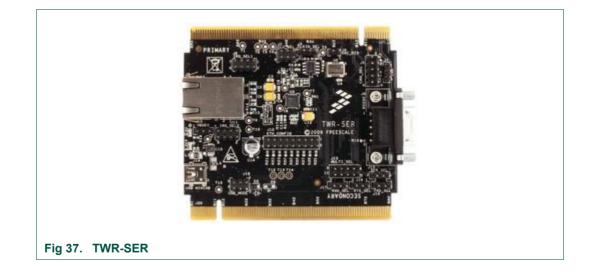
# The probe and the adapter are not part of the kit. They have to be purchased separately.

The first time the probe is used, its driver has to be installed. Driver and instructions can be found under JLINK website: choose the "J-Link Software and Documentation Pack" download:

https://www.segger.com/downloads/jlink

### 4.2.2 Debug information (printf)

Some debug information can be received through UART, using a Serial board extension.



This board is not part of the kit, but can be purchased directly from NXP: <u>TWR-SER NXP Web page</u>

In order to use this board and display the printf messages from the software, the TWR-SER module has to be inserted in the POS-RDR-SLN Kit. As for the other modules, the primary and secondary interfaces have to match the right side of the TWR-ELEV boards.



Once inserted, the Serial port seen in Fig 38 has to be connected to a computer COM port. On the computer, open a COM port terminal, with a speed of 115200 bps, and the data will be displayed as the software runs.

## 4.2.3 Using IAR

This chapter describes how to open and run the project Payment Application Demo.

IAR must be pre-installed with a valid license before going through these steps.

The minimum required IAR version is 7.70.0.

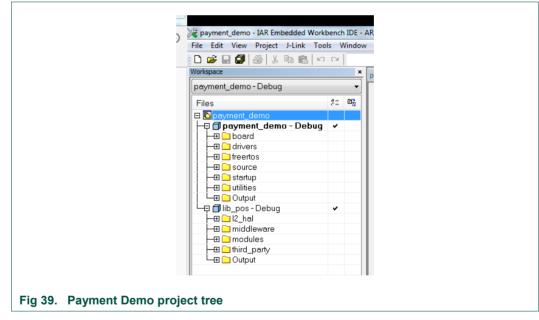
#### 4.2.3.1 Open the project and compile

- 1. Locate the .eww file from the demo project folder:
  - K81POSCR\_SW\_Release\boards\twrposk81\demo\_apps\payment\_demo\iar
- 2. Double click on the .eww file. It will open the project in IAR.

Alternatively, if the .eww files are not linked to IAR, the following step have to be done. Otherwise, jump to step 6.

- 3. Open IAR
- 4. Select File>Open>Project and browse to the folder containing the IAR project file (extension is .eww).
- 5. Select the file payment\_demo.eww

The project contains two subprojects: Payment\_demo and lib\_pos:



The Payment\_demo project is the main application. It requires the lib\_pos library to compile.

The compilation must then start with this lib\_pos project.

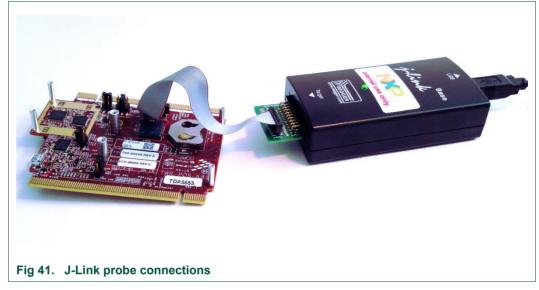
6. Right click on the lib\_pos project, and select Make

🗅 🚅 🖬 🕼 🎒 🕹 🕹 🗈		• 5
Workspace	× payment_	demo.c task.h tas
payment_demo - Debug	•	HAL_ClearF
Files	2: B	HAL_Append
🗆 🖸 payment_demo		<pre>}else if(host     break;</pre>
	Debua 🗸	}
board		}
📕 📙 🗀 drivers	1	return;
freertos	}	
	Ė /*	,
H H ⊡ startup		@brief Applica
U I I III ities U I IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	*	
└-₽ 🗗 lib_pos - Debug	int	t main(void)
	Options	
🕀 🗀 middleware	Mala	config_t
- 🕀 🗀 modules	Make	it board
third_party	Compile	InitPin
🖵 🖵 🗀 Output	Rebuild All	BootClo
	Clean	InitDek E BENCHN
	C-STAT Static Analysis	nit time
	Stop Build	TAMP_Ini time = T
	Add	itialize
	Remove	SetDefau Init(DMA
	Rename	
	Version Control System	nitializ M_Enable DV. Kerks
	Open Containing Folder	DK Worka rst, inv
Overview payment_dem	File Properties	
× Log	Set as Active	

- Once the lib\_pos library is compiled, a library lib\_pos.a must be found in K81POSCR\_SW\_Release\boards\twrposk81\demo\_apps\payment\_demo\iar\lib\_ pos\debug (if debug configuration mode is used)
- 8. Build the Payment\_demo app by right-clicking on the payment\_demo solution, and choosing "Make".
- 9. The software is now built and can be loaded into the POS Reader Solution K81 target.

### 4.2.3.2 Download Software

Downloading Software requires the J-Link probe to be connected to the computer over USB, and to the TWR-POS-K81 module, through the 19-pin Adapter. See Fig 41.



The J-Link probe has to be connected to the connector shown in Fig 42.



With the probe connected and the kit powered, select Project >Download>Download Active Application in IAR.

Note: The K81 must be powered to perform this application.

File Edit	View Proj	ect J-Link Tools Window	Help	
🗅 🗳 日		Add Files		- 🗸 🏷 🌾 🏋 🛐 🖻 🐢 📣 🚱 🔯
Workspace		Add Group		k.h   tasks.c   timers.c   comm_if_module.c   pin_module
payment_demo Import File List			arFile(fileName);	
Files Add Project Connection				<pre>&gt;endData2File(fileName, &amp;hostReplyMsg[5-</pre>
🗆 🖸 payn	ent d	Edit Configurations		[hostReplyMsg[0]==0x8E&&hostReplyMsg[1]
□ <b>]</b> pε	nyme	Remove		
	board	Keniove		
│┝─⊞҇і		Create New Project		
		Add Existing Project		
-⊕ 🗀		Options	ALT+F7	plication entry point.
		· · · · · · · · · · ·		fication entry point.
<b>L⊕ <u>C</u>C L⊋ <b>[]</b>∥b_</b>		Version Control System	+	1)
	l2_hə	Make	F7	ig t edmaConfig;
		Compile	CTRL+F7	.rg_b candomrig,
		Rebuild All		poard hardware. */
 	_	Clean		<pre>.tPins(); &gt;tClockRUN();</pre>
		Batch build	F8	.tDebugConsole();
		C-STAT Static Analysis	•	<pre>INCHMARK == 1) timestamp. */</pre>
		-		<pre>int();</pre>
		Stop Build	CTRL+ATTN	= TIMESTAMP_GetTimeStamp64();
		Download and Debug	CTRL+D	lize the eDMA. */
		Debug without Downloading		<pre>pefaultConfig(sedmaConfig);</pre>
		Attach to Running Target		;(DMA0, sedmaConfig);
		Make & Restart Debugger	CTRL+R	alize the Code Cache. */
		Restart Debugger	CTRL+MAJ+R	ableCodeCache(LMEM, true);
		Download	۰.	Download active application
		SFR Setup		Download file
				Erase memory
Overview Pay Structure Construction File				lize the System Cache. */
Overview	-puj	Save List of Registers		

User manual COMPANY PUBLIC

### 4.2.3.3 Debug Software

To run the software in debug mode, the same J-Link probe can be used with the same connection.

To start the debug mode, simply click the debug button or select Project>Download and Debug:

File Edit View Pr	oject J-Link Tools Window	Help		
🗅 🚅 🔚 🞒	Add Files		✓ ✓ > > > 2 D > > >	
Norkspace	Add Group		<b>`</b>	
payment_demo	Import File List			
Files	Add Project Connection			
🗆 🖸 payment_d	Edit Configurations			
	Remove		- 0;	
—⊞ 🗀 drive	Create New Project		FileIndexNumber	
-⊞ 🔁 freert	Add Existing Project			
-⊞ istartu	Options	ALT+F7	dMsg,2,10,hostReplyMsg,&hostReplyLen);	
🗕 🕀 🗀 utilitie		ALITI	EsshostReplyMsg[1]==0x01) {	
└─⊞ 🗀 Outpi └─₽ 🗇 lib_pos	Version Control System	+	<pre>isg.hostReplyLen-2, &amp;CRC[0], &amp;CRC[1]);</pre>	
—————————————————————————————————————	Make	F7	<pre>isg[hostReplyLen-2]  CRC[1]!=hostReplyMsg[hostReplyLen-1]) {     ERROR);</pre>	
🕀 🗀 midd	Compile	CTRL+F7	(_ERKOR);	
–⊞ 🗀 modu –⊞ 🗀 third	Rebuild All			
L 🖽 🛄 Outpi	Clean		ReplyMsg[2]; short)(((hostReplyMsg[fileNameLength+3]<<8)s0x00FF00) hostReplyN	
	Batch build	F8	;	
	C-STAT Static Analysis	•	<pre>ReplyMsg[3],fileNameLength); e);</pre>	
	Stop Build	CTRL+ATTN	ileName, shostReplyMsg[5+fileNameLength], configFileLength);	
			]==0x8E&&hostReplyMsg[1]==0x00) {	
<	Download and Debug	CTRL+D	2	
	Debug without Downloading			
	Attach to Running Target Make & Restart Debugger	CTRL+R		
	Restart Debugger	CTRL+R CTRL+MAJ+R		
	Download	CTRL+IMAU+R	r point.	
		,		
	SFR Setup			
	Open Device Description File	•	.g;	
	Save List of Registers		*/	
		InitPins();		
		BootClockRUN(); InitDebugConsol	=();	
		5		

## 4.2.4 Using KDS

#### 4.2.4.1 Install and start KDS

KDS (Kinetis Design Studio) has to be installed first. The KDS installation can be found from NXP website:

www.nxp.com/products/software-and-tools/run-time-software/kinetis-software-andtools/ides-for-kinetis-mcus/kinetis-design-studio-integrated-development-environmentide:KDS\_IDE\_

#### The minimum required KDS version is 3.2.0.

Once KDS is installed, launch the application. KDS first asks to select a folder that will become the Workspace for this KDS session.

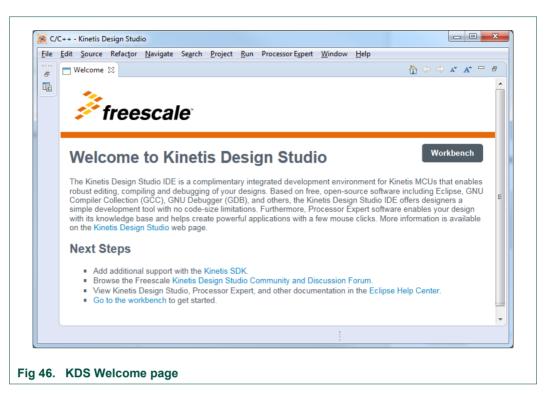
Select any folder on the local disk. It doesn't have to contain data at first.

Remember the folder location: next time KDS will be open, this workspace will have to be selected to retrieve the ongoing projects. The workspace will not necessarily contain source code, but it will contain all configuration and information about current projects.

<u>W</u> orkspace:	C:\SLN-POS-RDR\Workspace	•	<u>B</u> rowse
Use this a	s the default and do not ask again	ОК	Cancel
45 Solo	ct KDS Workspace	UK	Cancel

When the workspace path is selected, KDS opens with its welcome information page:

User manual COMPANY PUBLIC



Close this page, and KDS will show its default view:

📑 🕶 📙 🕒 🗎 🗞 🕶 🔂 🕶	Search Project Run Processor	· 9: ▼ 9: ▼   ∞   ⊗   ≯   @	😂 🖋 🕶 🗉 🗉	1 k + P + +	(+) + (+) +
				Quick Access	🖻 🖻 🖬 C/C
🎦 Project Explorer 👷 📃 🗖			🗆 🗖 🔡 Outlin	e 🔀 🛞 Make	🗐 Task L 🗖
			An outline	is not available.	69
	Problems 🕄 🔎 Tasks 📮	Console 🔲 Properties			
	0 items	Console 🔲 Properties			
		Console Properties	Path	Location	type
	0 items		Path	Location	
	0 items		Path	Location	
	0 items		Path	Location	
0 items selected	0 items		Path	Location	

### 4.2.4.2 Import the project and compile

From here, the Payment\_demo application project can be imported, following below steps:

- 1. Select in the menu: File>Import
- 2. In the 'Import' Window, select 'General', then 'Existing Project into Workspace'

Select Create new projects from an archive file or directory.	
type filter text	
(2) < <u>Back</u> <u>Next &gt;</u> <u>Finish</u>	Cancel

- 3. Click 'Next'
- 4. Check 'Select root directory', and Browse to the following folder (or enter the path to the following folder in the path field:

K81POSCR\_SW\_Release\boards\twrposk81\demo\_apps\payment\_demo\kds

5. Then press 'Enter'. The existing projects will be shown in the below window:

Mimport	A STATE OF A STATE OF A STATE	
Import Projects		
Select a directory to sear	ch for existing Eclipse projects.	
Select root directory:	$\label{eq:sw_Release} $$ SW_Release\boards\twrposk81\demo_apps\payment_demo\kds $$ areas and $$ areas area$	▼ B <u>r</u> owse
Select <u>archive file</u> :		▼ B <u>r</u> owse
<u>P</u> rojects:		
	\NFC\RefDesign\POS_FSL\SW\K81POSCR_SW_Release\boards\twrpo C:\Work\NFC\RefDesign\POS_FSL\SW\K81POSCR_SW_Release\board	
< Options	m	Þ
Searc <u>h</u> for nested pro	-	
Copy projects into w Hide projects that all	orkspace eady exist in the workspace	
Working sets		
Add projec <u>t</u> to work	ing sets	
W <u>o</u> rking sets:		S <u>e</u> lect
?	< <u>B</u> ack <u>N</u> ext > <u>Finish</u>	Cancel
g 49. Projects to i	nport from payment-demo application	

- 6. Select the two projects (payment\_demo and lib\_pos), make sure the option boxes are not checked, and click Finish.
- 7. Projects are now imported

C/C++ - Kinetis Design Studio				1.04		
ile <u>E</u> dit <u>Source</u> Refac <u>tor</u> <u>N</u> avigate Se <u>a</u> rch <u>P</u> roject <u>R</u> un						
* -       🖕    🗞 + 🗞 + 🖬    👉 + 🎯 + 🖸 + 🧭	▼: 捺 ▼ 🖸 ▼ 🖓 ▼ 🏰 ▼: 🏹   🖑 : 🗲 : 🕮 🖉	9 🖋 ▼ : L				
			(	Quick Access	<b>B</b>	C/C+
🗅 Project Explorer 🙁 📄 🖙 🗢 🗖			E Ou	🔀 🖲 M	. 🔲 Т	- 6
<ul> <li>∠ ∑ lib_pos</li> <li>&gt; ∑ Includes</li> <li>&gt; Z lal</li> <li>&gt; midleware</li> </ul>			An out	line is not avail	able.	9 9
<ul> <li>▷ chind party</li> </ul>						
payment_demo_twrposk81 debug jlink.launch payment_demo_twrposk81 release jlink.launch	🖹 Problems 🔀 🧔 Tasks 📃 Console 📃 Pro	perties			69	~ - 6
payment_demo_twiposkor release jiink.ladiren	0 items					
	Description	Resou	rce	Path		Location
	« [					
tems selected	* m					

8. Projects can now be compiled: Right click on 'lib\_pos' projects in the left panel, and select 'build'

	urce Refac <u>t</u> or <u>N</u> avigate		
<b></b>	📥   🛞 🕶 🚳 🗖 🛅	₫ ▼ ᡋᡱ ▼ 🗗 ▼ 😅 ▼	* • O •
🍋 Project Exp	lorer 🛛		
⊿ 😂 lik	New		•
▷ 🖻 ▷ 👉	Go Into		
Þ 🔄	Open in New Window		
	Сору		Ctrl+C
⊿ 😂 pa	Paste		Ctrl+V
Þ 🖻 🗙	Delete		Delete
▷ 🚰 🐣	Remove from Context	Ctrl+Alt+Sh	ift+Down
▷ 🔄	Source		+
⊳ <b>6</b>	Move		
▷ 🔄 ▷ 🔄	Rename		F2
	Import		
	Export		
	Build Project		
	Clean Project		
	Export Build Project Clean Project		FT

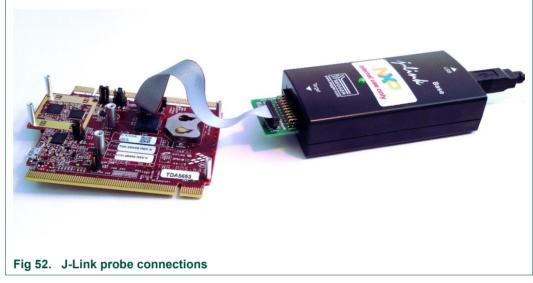
- 9. When the build is completed, do the same with the payment\_demo project.

Note that the compilation order is important: lib\_pos is a library which is needed by the payment\_demo application. If the payment\_demo application project is compiled first, the compilation may fail, or the project wouldn't include the latest changes from lib\_pos.

Once both builds are done, the full project is compiled and ready to use.

#### 4.2.4.3 Download Software and debug

Downloading Software requires the J-Link probe to be connected to the computer over USB, and to the TWR-POS-K81 module, through the 19-pin Adapter. See Fig 52.



The J-Link probe has to be connected to the connector shown in Fig 42.

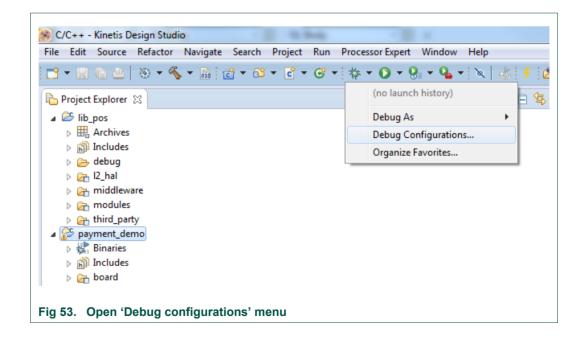
User manual

**COMPANY PUBLIC** 



### Note: The K81 must be powered to perform this application.

To download the software through the debug mode, open the debug configurations menu by clicking the little bug in the menu bar and selecting "Debug Configurations...":



In the menu, select "payment\_demo\_twrposk81 debug jlink", and click debug:

ate, manage, and run configurations	
Image: Second Secon	Name: payment_demo_twrposk&l debug Jink  Main Debugger Startup Source Common Project: payment_demo C/C++ Application: debug fayment_demo.eff Build (offiguration: debug before launching Build configuration: debug Use workspace settings Configure Workspace Settings
ter matched 10 of 11 items	ApplyRevert
<u>?</u>	Debug Close

The software will be loaded into K81 through the J-Link probe, and KDS will change its perspective to the debug view.

Press the F8 key to launch the program, or press F5 to run step by step.

User manual COMPANY PUBLIC

## 4.3 PN5180 Firmware update procedure

## 4.3.1 Introduction

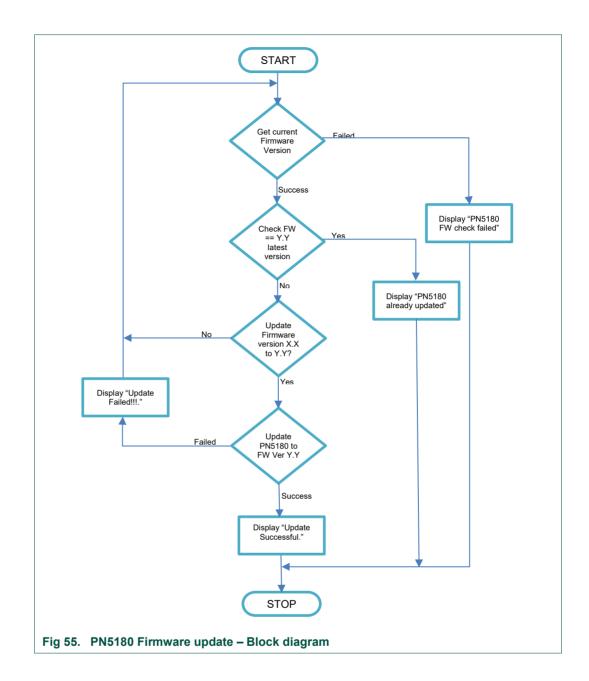
The TWR-POS-PN5180 board includes a contactless reader device which embeds a controller and its own Firmware for contactless operations.

In some cases, this FW has to be upgraded – For instance when a new EMVCo specification has to be supported.

This chapter describes the PN5180 secure firmware update application. Application allows customer to update PN5180 firmware to the latest firmware version. pn5180\_firmware\_update will check the onboard PN5180 firmware and notifies the user to update to the latest version if needed. Hardware modifications are required only for existing/legacy TWR-POS boards already in the field to perform firmware update which will be described in detail in the further sections.

## 4.3.2 Secure Firmware Update Flow Diagram

The next figure shows the PN5180 secure FW update application flow.



The above block diagram shows the pn5180\_firmware\_update application flow. Firmware version of onboard PN5180 is compared with the latest firmware version which is available for download. If firmware versions are the same, graphic LCD is updated with a UI message saying that "PN5180 already updated" and stops the application. If firmware versions are not the same, prints a UI message "Update Firmware version X.X to Y.Y?" (Whereas X.X is current version and Y.Y is the version to update to). In case user confirms by pressing "OK" on graphic LCD then application performs PN5180 firmware update and prints out status message like "Update successful" or "Update Failed!!!".

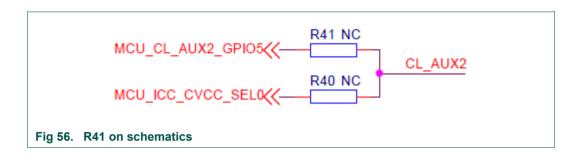
## 4.4 Hardware Modifications

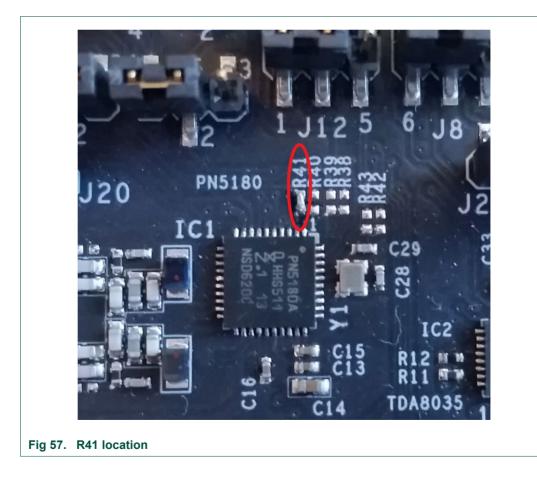
Chapters 4.4.1 and 4.4.2 describe board modifications on TWR-POS-PN5180 and TWR-POS-K81 boards allowing download of PN5180 Firmware.

POS kits delivered after 8 February 2017 already include these changes.

## 4.4.1 TWR-POS-PN5180 Board Modification

Current TWR-POS-PN5180 board includes NC (Non Connected) R41 as shown in below schematic. Add 0 Ohm resistor in place of "R41 NC". See the below board image to identify the R41 resistor placing.

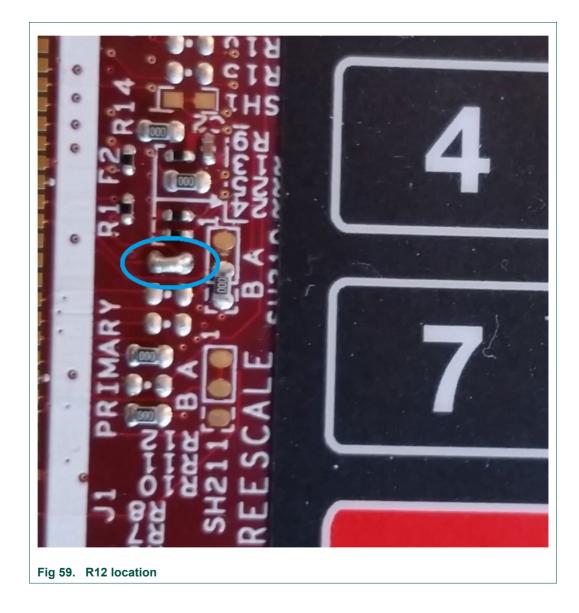




## 4.4.2 K81 Mother Board Modification

Current K81 mother board includes R12 DNP as shown in the snap below.





## 4.5 PN5180 firmware update

## 4.5.1 PN5180 Latest Firmware update

Make sure the two mandatory hardware changes are done as mentioned above before downloading pn5180\_firmware\_update application into the board.

The "demo\_apps" folder contains the pn5180\_firmware\_update example project that is prepared for two IDEs: IAR and KDS. Build and load the application into the board with the chosen IDE.

For more details on how to build and load the project, refer to section 4.2.3 for IAR or 4.2.4 for KDS. The only difference is the name of the main project: replace "payment\_demo" by "secure\_fw\_update".

Upon executing pn5180\_firmware\_update will check the current firmware version in PN5180.

- 1. If the current version is already latest, then no more action required and graphic LCD is displayed with "**PN5180 already updated**".
- 2. If the current version is NOT latest, then graphic LCD is displayed with "**Update Firmware version X.X to Y.Y?**".

	PN5180 Firmware Update
	Update Firmware version 3.5 to 3.9
	OK
Fig 60.	Firmware update screen

Press "OK" to update PN5180.



PN5180 will be updated to the latest 3.9 version and "**Update Successful**" is displayed on graphic LCD.

Restarting the board again should result in "PN5180 already updated"

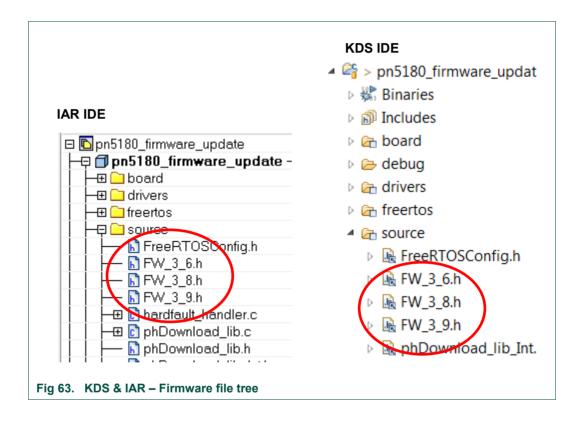


## 4.6 Add New PN5180 firmware version

## 4.6.1 Adding new PN5180 firmware to pn5180\_firmware\_update

Minor changes are required to add a new PN5180 firmware version support in pn5180\_firmware\_update application. The "demo\_apps" folder contains the pn5180\_firmware\_update example project that is prepared for two IDEs: IAR and KDS.

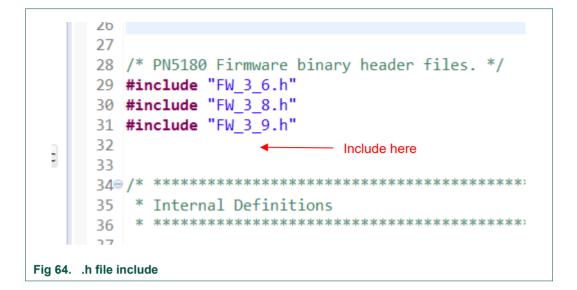
1. Add PN5180 FW\_Y\_Y.h header file to the pn5180\_firmware\_update project with the chosen IDE, similar to the image shown below for FW\_3\_6.h, FW\_3\_8.h & FW\_3\_9.h versions.



2. Include the FW\_Y\_Y.h in pn5180\_firmware\_update\_app.c as indicate below.

User manual

**COMPANY PUBLIC** 



3. Add a macro define for the new firmware version in pn5180\_firmware\_update.h, similar macro defines are shown below.

<pre>39 40 /* List of available firm 41 #define PN5180_FW_VER_3_6 42 #define PN5180_FW_VER_3_8 43 #define PN5180_FW_VER_3_9 44 45</pre>	<pre>are versions. */ 0x36 /* PN5180 firmware version 3.6 */ 0x38 /* PN5180 firmware version 3.8 */ 0x39 /* PN5180 firmware version 3.9 */ Add macro define here</pre>	
44		

If the firmware version is Y.Y assign macro value as 0xYY for example if the version is 4.0 then assign macro value as 0x40.

4. Assign the defined macro to PN5180\_FW\_VER in pn5180\_firmware\_update.h file similar to the image shown below.



 PN5180 new firmware header file FW\_Y\_Y.h will contain an array and its length. Assign array to PN5180\_FW\_VER\_BIN\_ARRAY macro and the length variable to PN5180\_FW\_VER\_BIN\_LEN in pn5180\_firmware\_update.h file as shown below.



By this all the changes are done, build the lib\_pos and pn5180\_firmware\_update projects and load the application into the board. To update PN5180 firmware to latest Y.Y version please refer to section 4.5 (PN5180 firmware update).

User manual COMPANY PUBLIC

## 5. References

- [1] K81 POS Card Reader Solution User's Guide.pdf
- [2] L2 Kernel Card Profiles and Demo Scenarios 21102016 v2.4.pdf

## 6. Legal information

## 6.1 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

## 6.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

**Evaluation products** — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer.

In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages.

Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

## 6.3 Trademarks

Notice: All referenced brands, product names, service names and trademarks are property of their respective owners.

User manual

**COMPANY PUBLIC** 

# 7. List of figures

Fig 1.	POS Reader Solution Kit box3
Fig 2.	Kit Content4
Fig 3.	TWR-POS-K815
Fig 4.	TWR-POS-PN51806
Fig 5.	TWR-LCD8
Fig 6.	TWR-Elev8
Fig 7.	Sample card9
Fig 8.	TWR-LCD connected on TWR-Elev primary10
Fig 9.	TWR-POS-K81 and TWR-POS-PN5180 sides11
Fig 10.	SLN-POS-RDR Hardware mounted12
Fig 11.	Battery location13
Fig 12.	Battery position13
Fig 13.	Connections for demo operations14
Fig 14.	Connector location – Micro USB14
Fig 15.	Power supply connector location – Mini USB. 15
Fig 16.	TWR-ELEV – Power switch ON position15
Fig 17.	Uninstalled COM port driver17
Fig 18.	Select proposed driver for Virtual COM port 17
Fig 19.	Installed driver18
Fig 20.	LCD Screen - Calibration #119
Fig 21.	LCD Screen - Calibration #219
Fig 22.	Issuer Host Simulation tool window20
Fig 23.	Demo start screen21
Fig 24.	Enter amount screen22
Fig 25.	Pin pad - Enter amount22
Fig 26.	Contact card insertion23
Fig 27.	Online approved24
Fig 28.	IHS - Transaction details24
Fig 29.	Offline approved25
Fig 30.	Contactless card tap25
Fig 31.	Offline approved26
Fig 32.	Factory reset #127
Fig 33.	Factory reset #227
Fig 34.	Factory reset #328
Fig 35.	Software Delivery – Folder tree29
Fig 36.	TWR-SER
Fig 37.	TWR-SER in the POS-RDR-SLN Kit31
Fig 38.	Payment Demo project tree32
Fig 39.	Start compiling – lib_pos
Fig 40.	J-Link probe connections
Fig 41.	J-Link connector location on TWR-POS-K81 bottom side
Fig 42.	IAR Download application35

Fig 43.	IAR Launch debug mode	.36
Fig 44.	Select KDS Workspace	.37
Fig 45.	KDS Welcome page	.38
Fig 46.	KDS default view – No project	.38
Fig 47.	Import Project	.39
Fig 48.	Projects to import from payment-demo	40
-	application	
Fig 49.	Projects imported	
Fig 50.	Build lib_pos	
Fig 51.	J-Link probe connections	.42
Fig 52.	Open 'Debug configurations' menu	.43
Fig 53.	Start debug mode	.44
Fig 54.	PN5180 Firmware update – Block diagram	.46
Fig 55.	R41 on schematics	.47
Fig 56.	R41 location	.47
Fig 57.	R12 on schematics	.48
Fig 58.	R12 location	.48
Fig 59.	Firmware update screen	.49
Fig 60.	"Update Successful" message	.50
Fig 61.	"Already updated" message	.50
Fig 62.	KDS & IAR – Firmware file tree	.51
Fig 63.	.h file include	.52
Fig 64.	Add macro define	.52
Fig 65.	Assign the new FW version	.53
Fig 66.	Assign pointer and length	

## 8. Contents

1.	Introduction3
2.	POS Reader Solution kit4
2.1	Kit Content4
2.1.1	TWR-POS-K815
2.1.2	TWR-POS-PN51806
2.1.2.1	Presentation6
2.1.2.2	Jumper settings6
2.1.3	TWR-LCD7
2.1.4	TWR-Elev8
2.1.5	Accessories9
2.1.5.1	Cables9
2.1.5.2	Battery9
2.1.5.3	Sample Card9
2.2	Setup the kit9
2.2.1	TWR-Elev Primary and Secondary boards9
2.2.2	Assemble the Hardware10
2.3	USB Connections14
2.4	TWR-ELEV Power Switch15
3.	Demonstration16
3.1	Setup the environment16
3.1 3.2	Setup the environment16 First power-up of the kit19
3.1 3.2 3.3	Setup the environment
3.1 3.2 3.3 3.3.1	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20
3.1 3.2 3.3	Setup the environment
3.1 3.2 3.3 3.3.1	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20
3.1 3.2 3.3 3.3.1 3.4	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20Factory Reset27POS Reader Solution Software29Introduction29
3.1 3.2 3.3 3.3.1 3.4 <b>4</b> .	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20Factory Reset27POS Reader Solution Software29
3.1 3.2 3.3 3.3.1 3.4 <b>4.</b> 4.1 4.2 4.2.1	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20Factory Reset27POS Reader Solution Software29Introduction29Software project30Debug Probe – J-Link30
3.1 3.2 3.3 3.3.1 3.4 <b>4.</b> 4.1 4.2 4.2.1 4.2.2	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20Factory Reset27POS Reader Solution Software29Introduction29Software project30Debug Probe – J-Link30Debug information (printf)30
3.1 3.2 3.3 3.3.1 3.4 <b>4.</b> 4.1 4.2 4.2.1 4.2.2 4.2.2 4.2.3	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20Factory Reset27POS Reader Solution Software29Introduction29Software project30Debug Probe – J-Link30Using IAR32
3.1 3.2 3.3 3.3.1 3.4 <b>4.</b> 4.1 4.2 4.2.1 4.2.2 4.2.3 4.2.3.1	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20Factory Reset27POS Reader Solution Software29Introduction29Software project30Debug Probe – J-Link30Debug information (printf)30Using IAR32Open the project and compile32
3.1 3.2 3.3 3.3.1 3.4 <b>4.</b> 4.2 4.2.1 4.2.2 4.2.3 4.2.3.1 4.2.3.2	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20Factory Reset27POS Reader Solution Software29Introduction29Software project30Debug Probe – J-Link30Using IAR32Open the project and compile32Download Software34
3.1 3.2 3.3 3.3.1 3.4 <b>4.</b> 4.2 4.2.1 4.2.2 4.2.3 4.2.3.1 4.2.3.2 4.2.3.3	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20Factory Reset27POS Reader Solution Software29Introduction29Software project30Debug Probe – J-Link30Debug information (printf)30Using IAR32Open the project and compile32Download Software34Debug Software36
3.1 3.2 3.3 3.3.1 3.4 <b>4.</b> 4.2 4.2.1 4.2.2 4.2.3 4.2.3.1 4.2.3.2 4.2.3.3 4.2.4	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20Factory Reset27POS Reader Solution Software29Introduction29Software project30Debug Probe – J-Link30Using IAR32Open the project and compile32Download Software34Debug Software36Using KDS37
3.1 3.2 3.3 3.3.1 3.4 <b>4.</b> 4.2 4.2.1 4.2.2 4.2.3 4.2.3.1 4.2.3.2 4.2.3.3	Setup the environment16First power-up of the kit19Payment Demo application20Launch the demonstration20Factory Reset27POS Reader Solution Software29Introduction29Software project30Debug Probe – J-Link30Debug information (printf)30Using IAR32Open the project and compile32Download Software34Debug Software36

4.2.4.3	Download Software and debug	42
4.3	PN5180 Firmware update procedure	45
4.3.1	Introduction	45
4.3.2	Secure Firmware Update Flow Diagram	45
4.4	Hardware Modifications	47
4.4.1	TWR-POS-PN5180 Board Modification	47
4.4.2	K81 Mother Board Modification	48
4.5	PN5180 firmware update	49
4.5.1	PN5180 Latest Firmware update	49
4.6	Add New PN5180 firmware version	51
4.6.1	Adding new PN5180 firmware to	
	pn5180_firmware_update	51
5.	References	54
6.	Legal information	55
6.1	Definitions	55
6.2	Disclaimers	55
6.3	Trademarks	55
7.	List of figures	56
8.	Contents	57

Please be aware that important notices concerning this document and the product(s) described herein, have been included in the section 'Legal information'.

#### © NXP Semiconductors N.V. 2019.

All rights reserved.

For more information, please visit: http://www.nxp.com

Date of release: 27 May 2019