



# Occupancy Sensor Node Reference Design

## Board

### User's Guide

## 1. Introduction

This manual describes the NXP reference design board Occupancy Sensor Node. The Occupancy Sensor Node circuit board provides a diverse reference design with all necessary I/O connections to use as a self-contained board or for connection to an external application.

The Occupancy Sensor Node board is built around the NXP MKW24D512. The MKW24D512 wireless MCU is a 2.4 GHz Industrial, Scientific, and Medical (ISM) single-chip device intended for the IEEE® Std. 802.15.4, including Thread®, ZigBee Pro, ZigBee RF4CE, and IPv6/6LoWPAN protocols.

The Occupancy Sensor Node board contains the MKW24D512 transceiver that works in conjunction with a software stack to implement an IEEE Std. 802.15.4 platform solution.

### 1.1. Audience

This manual is intended for system designers and system engineers.

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## 2. Board overview and description

The Occupancy Sensor Node board is based on the NXP MKW24D512 transceiver (MKW24); it incorporates a complete low-power IEEE Std. 802.15.4, 2.4 GHz radio frequency transceiver. The Occupancy Sensor Node platform contains the MKW24D512 device with 32 MHz reference oscillator crystal, RF circuitry including antenna, a 6-axis sensor with integrated linear accelerometer and magnetometer, an EEPROM, an occupancy sensor and supporting circuitry in a reduced form factor board. The board is a standalone and supports applications development with the NXP IEEE Std. 802.15.4 protocol stack.

### 2.1. Board features

The Occupancy Sensor Node reference design board contains the MKW24D512 device and is used to demonstrate and evaluate the available features of the MKW24D512 in a reduced form factor board with a specific application taking advantage of the MKW24D512 low-power capabilities, USB module and NXP Combo sensor. It is powered by a Lithium-ion Rechargeable Cell Battery or via USB.

Figure 1 shows the Occupancy Sensor Node reference design board.

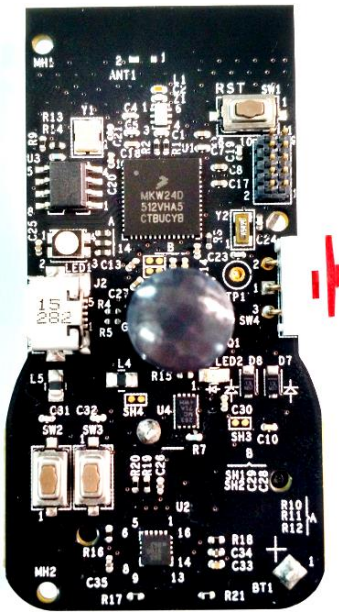


Figure 1. Occupancy Sensor Node reference design board

The Occupancy Sensor Node reference design board includes the following features:

- The NXP low-power Kinetis MKW24D512 transceiver
- Full IEEE Std. 802.15.4 compliant wireless node; ZigBee and Thread capable

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- Reference design area with small footprint, low-cost RF node
  - RF circuitry includes a Balun to convert the differential I/O pin of the MKW24D512 transceiver to single-ended for on-board signal routing
  - Low off-chip component count
  - Programmable output power from -35 dBm to +8 dBm
  - Receiver sensitivity: -102 dBm, typical (@1% PER for 20 byte payload packet)
- Integrated PCB meander horizontal antenna
- 32 MHz reference oscillator
- 32 kHz clock oscillator
- 2.4 GHz frequency operation (ISM Band)
- Cortex 10-pin (0.05 inch) JTAG/SWD debug port for target MCU
- 1 RGB LED indicator
- 2 interrupt push button switches
- 1 FXOS8700CQ combo sensor
- 1 Coin Cell battery holder
- 1 Occupancy Sensor
- 1 EEPROM
- 1 Battery Charger
- 1 micro-USB connector
- 1 On/Off switch

Figure 2 shows the main board features of the NXP Occupancy Sensor Node board.

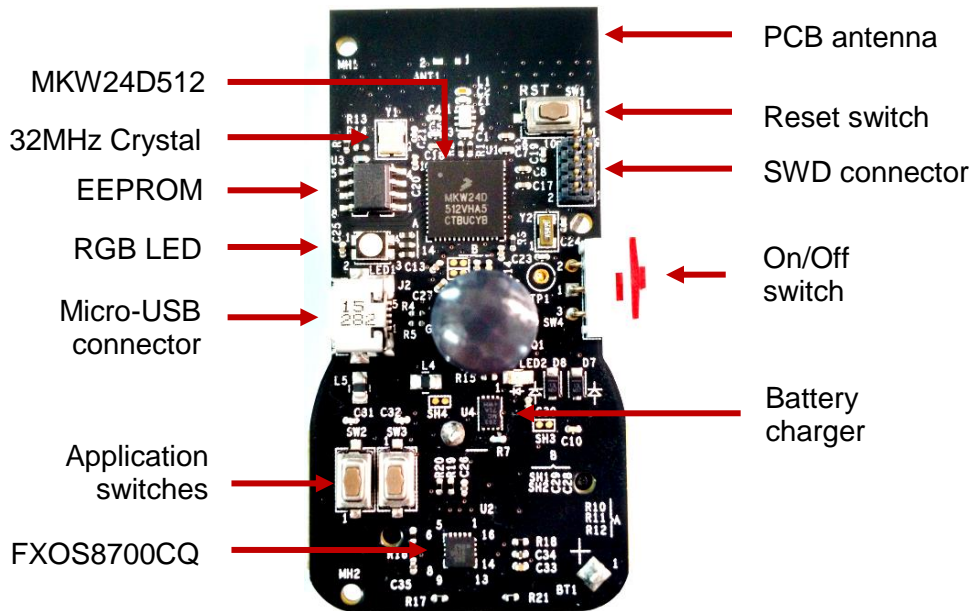


Figure 2. Occupancy Sensor Node reference design components

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### 3. Occupancy Sensor Node reference design board

The Occupancy Sensor Node board is a reference design based on the NXP MKW24D512 transceiver. The core device is accompanied by a 32 MHz reference oscillator crystal, RF circuitry including a PCB antenna (and supporting circuitry), accelerometer/magnetometer sensor, EEPROM, Occupancy Sensor and RGB LED in a small form factor board. [Figure 3](#) shows a simple block diagram.

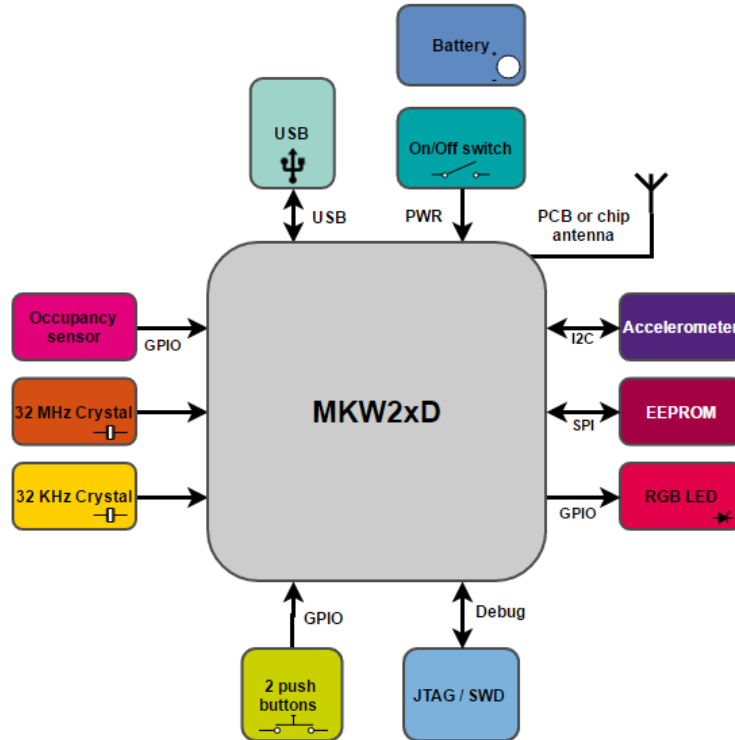


Figure 3. Occupancy Sensor Node block diagram

### 3.1. PCB Features

The Occupancy Sensor Node board provides the following features:

- 2-layer metal, 0.062 inch thick FR4 board
- LGA footprint and power supply bypass
- Integrated PCB meander horizontal antenna
- 32 MHz reference oscillator
- 32 kHz clock oscillator
- 2.4 GHz frequency operation (ISM Band)
- Cortex 10-pin (0.05 inch) JTAG/SWD debug port for target MCU
- 1 RGB LED indicator
- 2 interrupt push button switches
- 1 FXOS8700CQ combo sensor
- 1 Coin Cell battery holder
- 1 Occupancy Sensor
- 1 EEPROM
- 1 Battery Charger
- 1 micro-USB connector
- 1 on/off switch

### 3.2. Functional description

The Occupancy Sensor Node board is built around the NXP MKW24D512 transceiver in a 63-pin (56-pin usable) LGA package. The MKW24D512 device features an IEEE Std. 802.15.4 radio frequency transceiver and a Kinetis family low-power, mixed-signal ARM® Cortex®-M4 MCU in a single package. This two-layer board is intended as a reference design platform and/or as a building block for application development. The principal purpose of the reference design board is to demonstrate some of the available features of the MKW24D512 transceiver in a reduced form factor board with a specific application taking advantage of the MKW24D512 transceiver's USB and low-power capabilities.

### 3.2.1. RF performance and considerations

The Occupancy Sensor Node reference design board includes a 1 mW nominal output PA with internal voltage controlled oscillator (VCO), integrated transmit/receive switch, on-board power supply regulation, and full spread-spectrum encoding and decoding. Key specifications for the MKW24D512 transceiver are:

- Programmable output power from -35 dBm to +8 dBm MCU output pins
- Typical sensitivity is -102 dBm (@1% PER for 20 byte payload packet)
- Frequency range is 2360 MHz to 2480 MHz
- Differential bidirectional RF I/O port with integrated transmit/receive switch
- Meander horizontal printed metal antenna for a small footprint, low-cost design
- The board features a low component count RF matching network with off-chip 1:1 Balun

The layout has provision for out-of-band signal suppression (components L1 and C2) if required. [Figure 4](#) shows the typical topology for the RF circuitry.

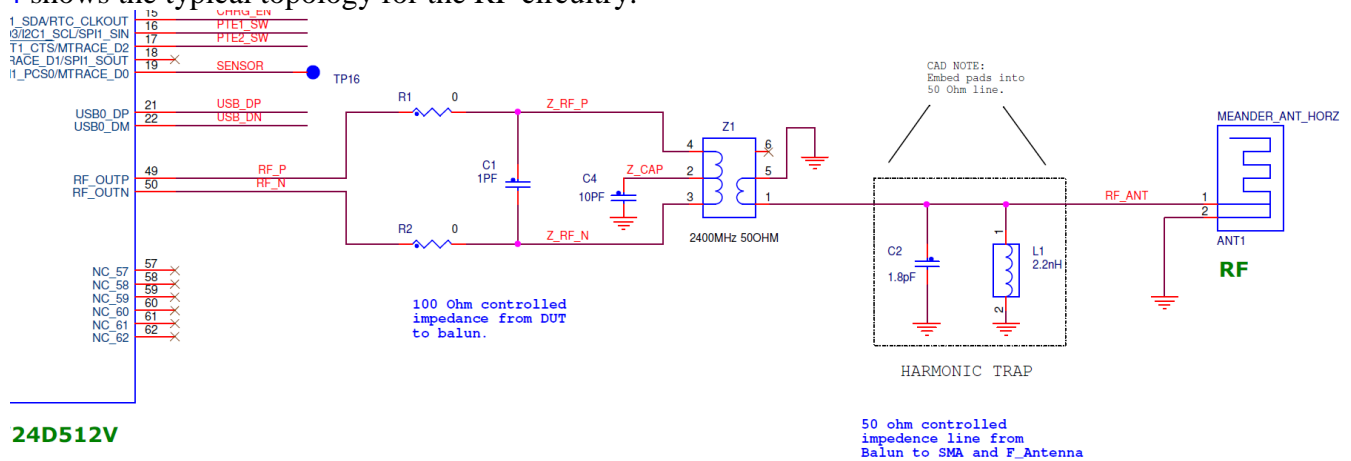
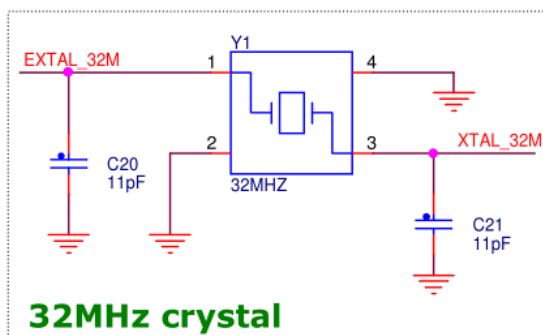


Figure 4. Occupancy Sensor Node RF circuitry

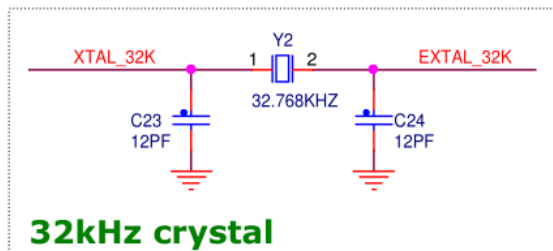
### 3.2.2. Clocks

The Occupancy Sensor Node has two clocks:

- 32 MHz Reference Oscillator: [Figure 5](#) shows the external 32 MHz external crystal Y1. This mounted crystal must meet the specifications outlined in the [AN3251](#) application note. The IEEE Std. 802.15.4 requires that the frequency be accurate to less than  $\pm 40$  ppm.
  - Capacitors C20 and C21 provide the bulk of the crystal load capacitance. At 25 °C it is desired to have the frequency accurate to  $\pm 10$  ppm or less to allow for temperature variation.
  - To measure the 32 MHz oscillator frequency, signal CLKOUT (PTA18/CLK\_OUT) can optionally be programmed to provide a buffered output clock signal.
- Optional 32.768 kHz Crystal Oscillator: Provision is also made for a secondary 32.768 kHz crystal Y2 (see [Figure 6](#)). This oscillator can be used for a low power accurate time base.
  - The module comes provided with this Y2 crystal and its load capacitors C23 and C24.
  - Load capacitors C23 and C24 provide the entire crystal load capacitance; there is no onboard trim capacitance.
  - The 32 kHz oscillator components are supplied.



**Figure 5. Occupancy Sensor Node 32 MHz reference oscillator circuit**



**Figure 6. Occupancy Sensor Node 32.768 kHz optional oscillator circuit**

### 3.2.3. Power management

The Occupancy Sensor Node power management circuit is shown in [Figure 7](#).

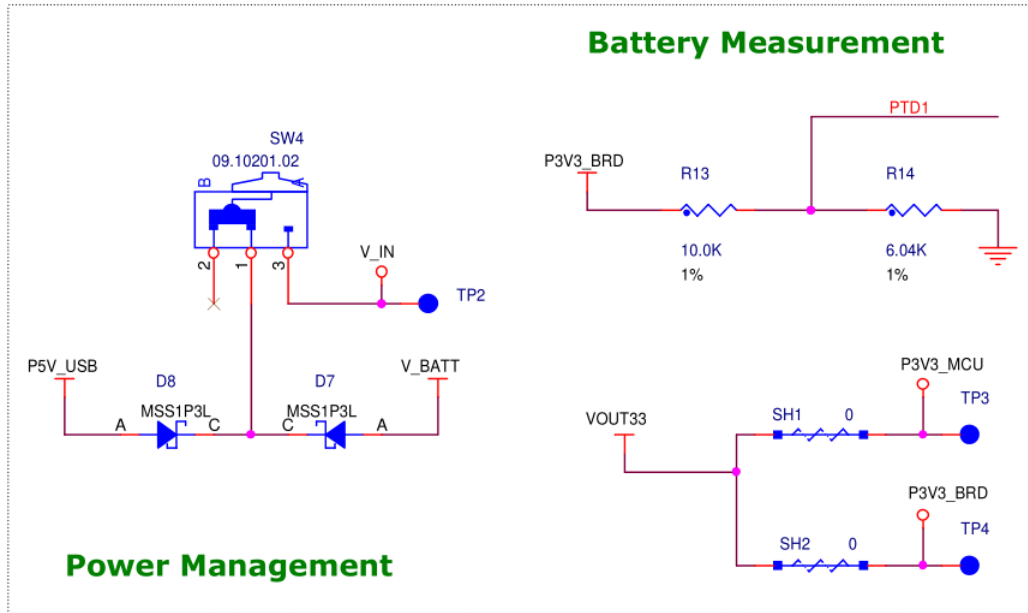


Figure 7. Occupancy Sensor Node power management circuit

### 3.2.4. Battery charger

The Occupancy Sensor Node battery charger circuit is shown in [Figure 8](#).



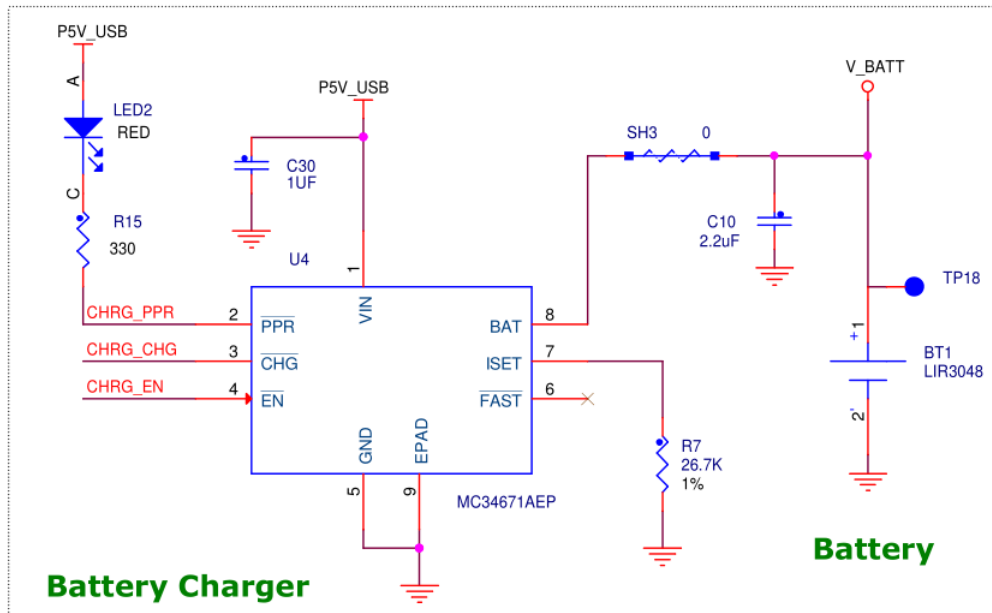


Figure 8. Occupancy Sensor Node battery charger circuit

### 3.2.5. USB connector

The Occupancy Sensor Node USB connector circuit is shown in Figure 9.

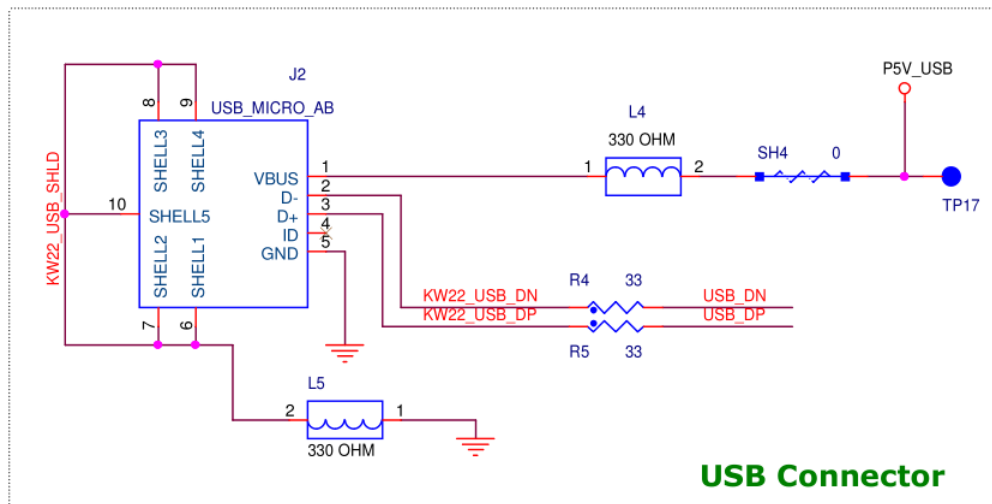


Figure 9. Occupancy Sensor Node USB connector circuit

### 3.2.6. External flash memory

The Occupancy Sensor Node External Flash Memory circuit is shown in Figure 10.

Occupancy Sensor Node reference design board

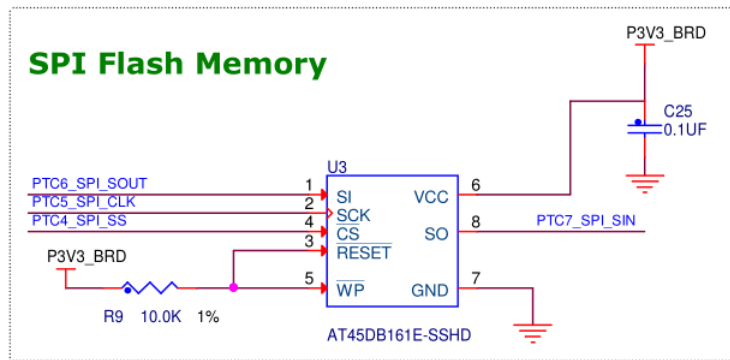


Figure 10. Occupancy Sensor Node External flash memory circuit

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### 3.2.7. Peripheral functions – Combo sensor (I<sup>2</sup>C interface)

Component U2 is a Freescale sensor, FXOS8700C, a 6-axis sensor with integrated linear accelerometer and magnetometer, very low power consumption, I<sup>2</sup>C selectable. Figure 11 shows the sensor circuit.

- Sensor power supply is P3V3\_BRD
- Discrete pull-up resistors for the I2C port are provided
- Two interrupt signals

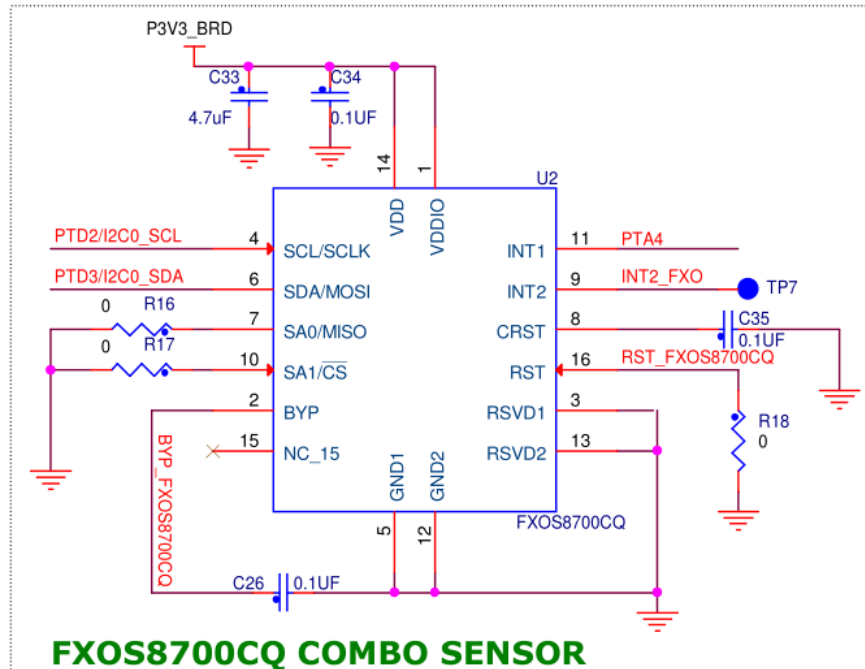


Figure 11. Occupancy Sensor Node FXOS8700CQ combo sensor

### 3.3. Schematic, board layout, and bill of material

#### 3.3.1. Schematic

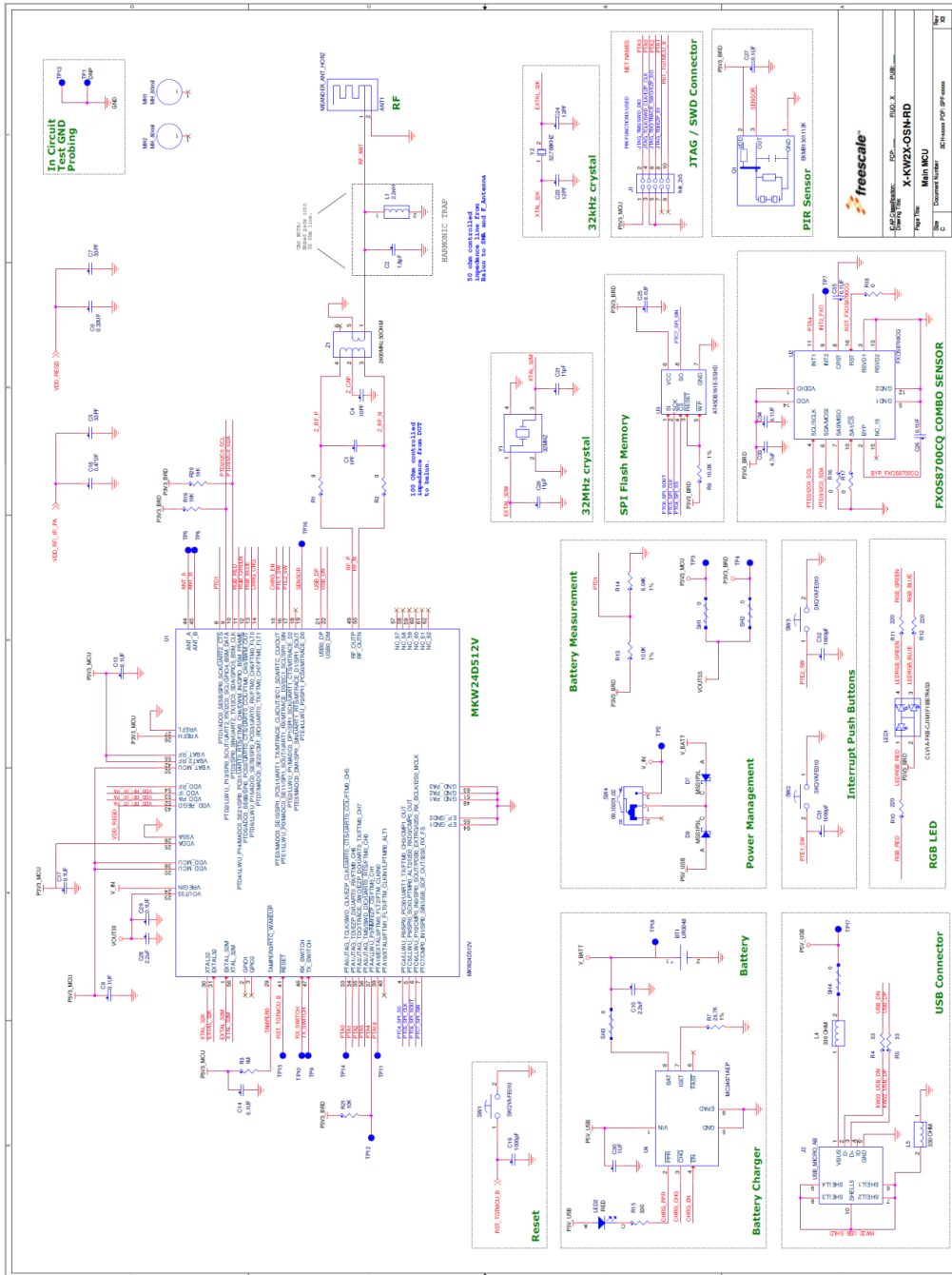


Figure 12. Occupancy Sensor Node schematic Rev X3

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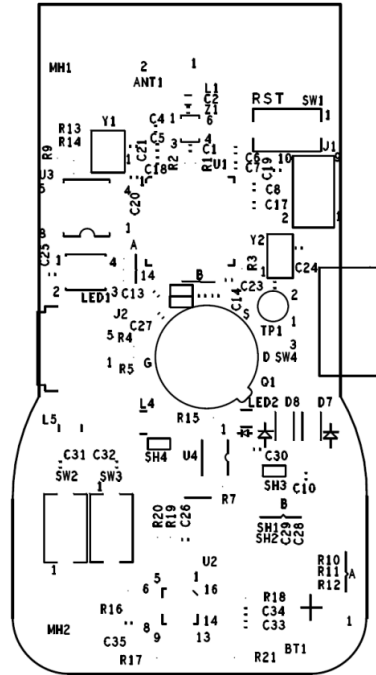


Figure 13. Occupancy Sensor Node board component location (top view)

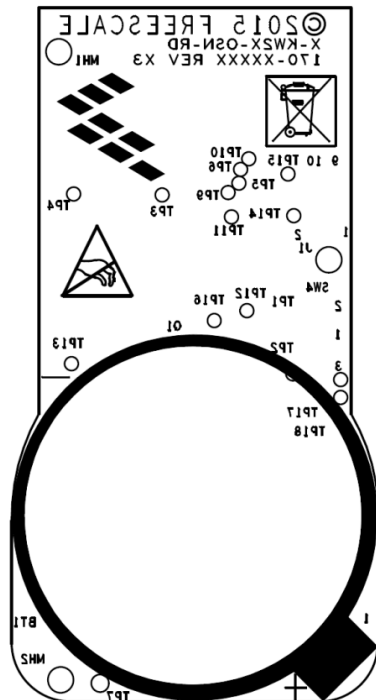


Figure 14. Occupancy Sensor Node board test points

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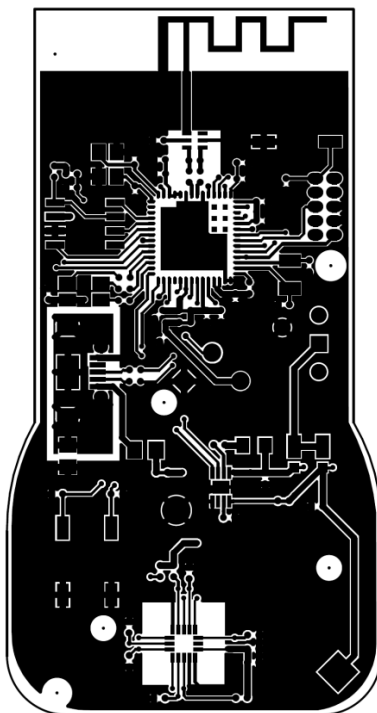


Figure 15. Occupancy Sensor Node board layout (top view)

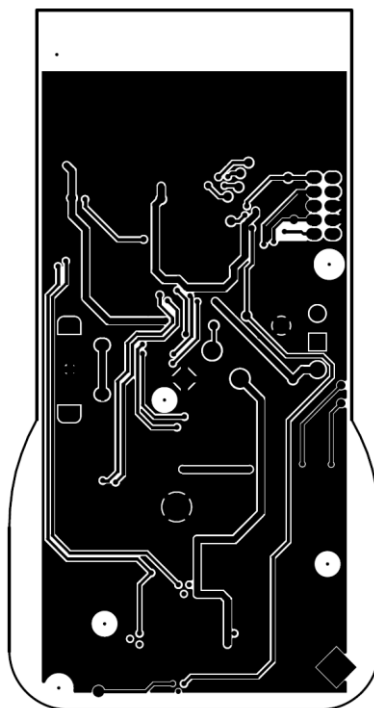


Figure 16. Occupancy Sensor Node board layout (bottom view)

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### 3.3.2. Bill of Materials

**Table 1. Bill of materials (common parts for all frequency bands)**

Item	Qty	Reference	Value	Description	Mfg. Name	Mfg. Part Number
1	1	ANT1	MEANDER_ANT_HORZ	PCB MEANDER ANTENNA HORIZONTAL, NO PART TO ORDER	NO PART TO ORDER	NO PART TO ORDER
2	1	BT1	LIR3048	BATTERY HOLDER 3032 HORIZONTAL TH	Keystone Electronics	301
3	1	C1	1PF	CAP CER 1PF 50V 5% COG 0402	MURATA	GRM1555C1H1R0CA01 B
4	1	C2	1.8pF	CAP CER 1.8PF 50V 0.25PF COG 0402	MURATA	GRM1555C1H1R8CA01 D
5	1	C4	10PF	CAP CER 10PF 50V 5% COG 0402	AVX	04025A100JAT2A
6	2	C5, C7	33PF	CAP CER 33PF 50V 5% COG 0402	VENKEL COMPANY	C0402C0G500-330JNE
7	1	C6	0.33UF	CAP CER 0.33UF 6.3V 10% X5R 0402	muRata	GRM155R60J334KE01 D
8	7	C8, C13, C14, C17, C25, C27, C29	0.1UF	CAP CER 0.1UF 16V 10% X7R 0402	KEMET	C0402C104K4RAC
9	2	C10, C28	2.2uF	CAP CER 2.2uF 6.3V 10% X5R 0402	MURATA	GRM155R60J225KE95
10	1	C18	0.47UF	CAP CER 0.47UF 6.3V 10% X5R 0402	Murata	GRM155R60J474KE19 D
11	3	C19,C31,C32	1000pF	CAP CER 1000PF 50V 5% COG 0402	MURATA	GRM1555C1H102JA01 D
12	2	C20,C21	11pF	CAP CER 11pF 50V 1% COG 0402	AVX	04025U110FAT2A
13	2	C23, C24	12PF	CAP CER 12PF 50V 5% COG 0402	MURATA	GRM1555C1H120JZ01 D
14	3	C26,C34,C35	0.1UF	CAP CER 0.1UF 10V 10% X5R 0402	KEMET	C0402C104K8PAC
15	1	C30	1UF	CAP CER 1UF 6.3V 20% X5R 0402	MURATA	GRM155R60J105ME19 D
16	1	C33	4.7uF	CAP CER 4.7UF 6.3V 20% X5R 0402	VENKEL COMPANY	C0402X5R6R3-475MNP
17	2	D7, D8	MSS1P3L	DIODE SCH 1A 20V MICROSMP SMT	VISHAY INTERTECHNOLOGY	MSS1P3L-M3/89A
18	1	J1	hdr_2x5	HDR 2X5 TH 50MIL CTR 254H AU 91L	SAMTEC	FTSH-105-04-F-D
19	1	J2	USB_MICRO_AB	CON 5 USB_MICRO_AB_RECEPTACLE RA SKT SMT 0.65MM SP 122H AU	molex	475900001
20	1	LED1	CLV1A-FKB-CJ1M1F1BB7R4S3	LED RED BL GRN SGL 50/25/25mA SMT	CREE, INC	CLV1A-FKB-CJ1M1F1BB7R4S3
21	1	LED2	RED	LED RED CLEAR SGL 30MA SMT 0805	LITE ON	LTST-C171KRKT
22	1	L1	2.2nH	IND -- 2.2NH@500MHZ 220mA 4% 0402	MURATA	LQP15MN2N2B02
23	2	L4, L5	330 OHM	IND FER BEAD 330OHM@100MHZ 2.5A -- SMT	TDK	MPZ2012S331A
24	2	MH1, MH2	MH_80mil	MOUNTING HOLE NON-PLATED 80MIL TH NO PART TO ORDER	NA	Mounting Hole - 80Mil drill NPTH

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### Occupancy Sensor Node reference design board

25	1	Q1	EKMB1301 112K	SENSOR TYPE PYROELECTRIC PIR Motion Sensor	Panasonic Corporation	EKMB1301112K
26	5	R1, R2, R16, R17, R18	0	RES MF ZERO OHM 1/16W 5% 0402	ROHM	MCR01MZPJ000
27	1	R3	1M	RES MF 1.0M 1/10W 5% 0402	PANASONIC	ERJ-2GEJ105X
28	2	R4, R5	33	RES MF 33 OHM 1/16W 5% 0402	SMEC	RC73L2Z330JTF
29	1	R7	26.7K	RES MF 26.7K 1/16W 1% 0402	KOA SPEER	RK73H1ETTP2672F
30	2	R9, R13	10.0K	RES MF 10.0K 1/16W 1% 0402	WALSIN TECHNOLOGY CORP.	WR04X1002FTL
31	3	R10, R11, R12	220	RES MF 220 OHM 1/16W 5% 0402	KOA SPEER	RK73B1ETTP221J
32	1	R14	6.04K	RES MF 6.04K 1/16W 1% 0402	KOA SPEER	RK73H1ETTP6041F
33	1	R15	330	RES MF 330 OHM 1/16W 5% 0402	VISHAY INTERTECHNOL OGY	CRCW0402330RJNED
34	3	R19,R20,R21	10K	RES MF 10K 1/16W 5% 0402	VISHAY INTERTECHNOL OGY	CRCW040210K0JNED
35	4	SH1,SH2,SH3, SH4	0	ZERO OHM CUT TRACE 0402 PADS; NO PART TO ORDER	LAYOUT ELEMENT ONLY	LAYOUT ELEMENT ONLY
36	3	SW1,SW2,SW 3	SKQYAFE01 0	SW SPST MOM PB 50MA 12V SMT	ALPS ELECTRIC (USA) INC.	SKQYAFE010
37	1	SW4	09.10201.0 2	SW SPDT RA SLD 12V 500MA TH	EAO SWITCH	09-10201-02
38	1	TP1	TEST POINT WHITE	TEST POINT WHITE 40 MIL DRILL 180 MIL TH 109L	COMPONENTS CORPORATION	TP-105-01-09
39	15	TP2,TP3,TP4, TP5,TP6,TP9, TP10,TP11,TP 12,TP13,TP14 ,TP15,TP16,T P17,TP18	TPAD_030	TEST POINT PAD 30MIL DIA SMT, NO PART TO ORDER	NOTACOMPON ENT	NOTACOMPONENT
40	1	TP7	TPAD_040	TEST POINT PAD 40MIL DIA SMT, NO PART TO ORDER	NOTACOMPON ENT	NOTACOMPONENT
41	1	U1	MKW22D5 12VHA5	IC MCU XCVR 2.4GHZ 64KB RAM 512KB FLASH - USB 1.8- 3.6V LGA56	Freescale Semiconductor	MKW22D512VHA5
42	1	U2	FXOS8700C Q	IC ACCELEROMETER AND MAGNETOMETER SENSOR 3-AXIS 2.5V QFN16	Freescale Semiconductor	FXOS8700CQ
43	1	U3	AT45DB161 E-SSHD	IC FLASH 16MBIT 85MHZ 2.5-3.6V SOIC8	ATMEL	AT45DB161E-SSHD-T
44	1	U4	MC34671A EP	IC CHARGER LI-ION BATT 24V DFN8	FREESCALE SEMICONDUCT OR	MC34671AEP
45	1	Y1	32MHZ	XTAL 32MHZ 9PF -- SMT 3.2X2.5MM	NDK	EXS00A-CS02368
46	1	Y2	32.768KHZ	XTAL 32.768KHZ SMT ROHS COMPLIANT	EPSON ELECTRONICS	FC-135 32.7680KA-A3
47	1	Y3	2400MHZ 500HM	XFMR BALUN 2400 +/-100MHZ SMT	MURATA	LDB212G4005C-001

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## 4. PCB manufacturing specifications

This section provides the specifications used to manufacture the Occupancy Sensor Node development printed circuit board (PCB) described in this guide.

The Occupancy Sensor Node development platform PCBs must comply with the following:

- The PCB must comply with Perfag1D/3C ([www.perfag.dk/en/](http://www.perfag.dk/en/))
- The PCB manufacturer's logo is required
- The PCB production week and year code is required
  - The manufacturer's logo and week/year code must be stamped on the back of the PCB solder mask
  - The PCB manufacturer cannot insert text on the PCB either in copper or in silkscreen without written permission from NXP Semiconductors.
- The required Underwriter's Laboratory (UL) Flammability Rating
  - The level is 94V-0 (<http://ulstandards.ul.com/standard/?id=94>)
  - The UL information must be stamped on the back of the PCB solder mask

### NOTE

- A complete set of design files is available for the Occupancy Sensor Node transceiver at the NXP website ([KW2xD](#)) under "Software and Tools." These reference designs should be used as a starting point for a custom application.
- The *Freescale IEEE 802.15.4 / ZigBee Package and Hardware Layout Considerations Reference Manual*, ([ZHDCRM](#)) is also available at the same web site to provide additional design guidance.

### 4.1. Single PCB construction

This section describes individual PCB construction details.

- The Occupancy Sensor Node PCBs are two-layer, multi-layer designs
- The PCBs contain no blind, buried, or micro vias
- PCB data:
  - Occupancy Sensor Node board's size: approximately 63.9 x 33.2 mm (2.52 x 1.31 inches)
  - Occupancy Sensor Node board's final thickness (Cu/Cu): 1.57 mm (0.62 inches)  $\pm 10\%$  (excluding solder mask)

[Table 2](#) defines some of the layers of the completed PCB. The artwork identification refers to the name of the layer in commonly used terms.

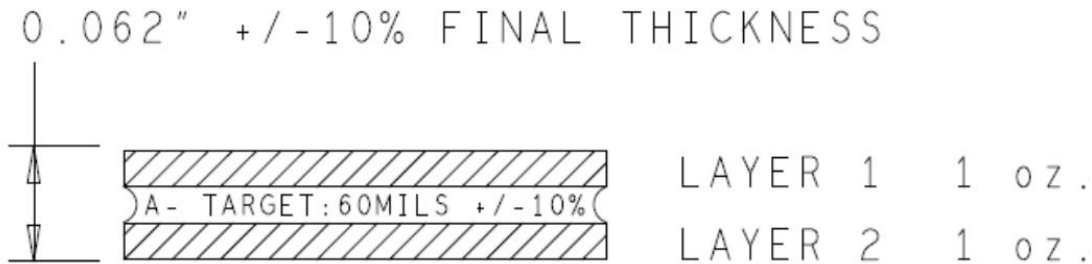
**Table 2. Occupancy Sensor Node layer by layer overview**

Layer	Artwork Identification	File Name
1	Silkscreen Top	PSS.art
2	Top Layer Metal	L1_PS.art
3	Bottom Layer Metal	L2_SS.art
4	Silkscreen Bottom	SSS.art

**CAUTION:**

The Occupancy Sensor Node reference design board contains high frequency 2.4 GHz RF circuitry. As a result, RF component placement, line geometries and layout, and spacing to the ground plane are critical parameters. As a result, BOARD STACKUP GEOMETRY IS CRITICAL.

Dielectric and copper thicknesses and spacing must not be changed; follow the stackup (see Figure 17) information provided with the reference design.



**Figure 17. Occupancy Sensor Node PCB stackup cross-section (two layer)**

- Solder mask is required
- Silk screen is required

## 4.2. Panelization

The panel size can be negotiated depending on production volume.

### 4.3. Materials

The PCB composite materials must meet the following requirements:

- Laminate: the base material (laminate) must be FR4. If the laminate material is changed, the RF electrical characteristics may change and degrade RF performance.
- Copper foil:
  - Top and Bottom copper layers must be 1 oz. copper
- Plating: All pad plating must be Hot Air Leveling (HAL).

### 4.4. Solder mask

The solder mask must meet the following requirements:

- Solder mask type: Liquid Film Electra EMP110 or equivalent
- Solder mask thickness: 10–30  $\mu\text{m}$ .

### 4.5. Silk screen

The silk screen must meet the following requirements:

- Silk screen color: White
- Silk screen must be applied after application of solder mask if solder mask is required
- The silk screen ink must not extend into any plated-thru-holes
- The silk screen must be clipped back to the line of resistance.

### 4.6. Electrical PCB testing

- All PCBs must be 100% tested for opens and shorts
- Impedance measurement: An impedance measurement report is not mandatory.

### 4.7. Packaging

Packaging for the PCBs must meet the following requirements:

- Finished PCBs must remain in panel
- Finished PCBs must be packed in plastic bags that do not contain silicones or sulphur materials.

These materials can degrade solderability.



## 4.8. Hole specification/tool table

See the *ncdrill-1-2.tap* file included with the Gerber files and the *FAB-KW2X-OSN-RD\_X3.pdf* file.

## 4.9. File description

Downloadable files include: Design, Gerber, and PDF files. Gerber files are RS-274x format. Not all files included with the Gerber files are for PCB manufacturing.

PDF files included are:

- *FAB-KW2X-OSN-RD\_X3.pdf*— board fabrication drawing
- *GRB-KW2X-OSN-RD\_X3.pdf*— metal layers, solder mask, solder paste and silk screen
- *SPF-KW2X-OSN-RD\_X3.pdf*— schematic diagram

Design files are in Allegro format with OrCAD schematic capture.

## 5. Revision history

Revision number	Date	Substantive changes
1	02/2017	Initial release