

SOT1533-2 LFBGA121, plastic, low profile fine-pitch ball grid array; 121 bumps; 0.65 mm pitch; 8 mm x 8 mm x 1.3 mm body 25 January 2019 Package information

Package information

Package summary 1

Terminal position code	B (bottom)
Package type descriptive code	LFBGA121
Package type industry code	LFBGA121
Package style descriptive code	LFBGA (low profile fine-pitch ball grid array)
Package body material type	P (plastic)
Mounting method type	S (surface mount)
Issue date	08-01-2016
Manufacturer package code	98ASA00423D

Table 1. Package summary

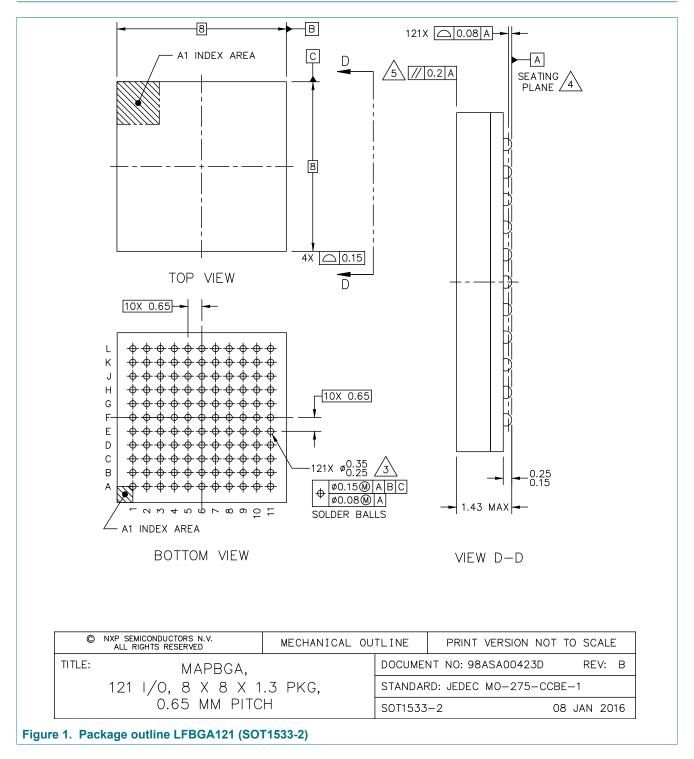
Parameter	Min	Nom	Мах	Unit
package length	-	8	-	mm
package width	-	8	-	mm
seated height	-	1.3	1.43	mm
nominal pitch	-	0.65	-	mm
actual quantity of termination	-	121	-	



SOT1533-2

LFBGA121, plastic, low profile fine-pitch ball grid array; 121 bumps; 0.65 mm pitch; 8 mm x 8 mm x 1.3 mm body

2 Package outline



SOT1533-2

LFBGA121, plastic, low profile fine-pitch ball grid array; 121 bumps; 0.65 mm pitch; 8 mm x 8 mm x 1.3 mm body

NOTES:

1.	ALL DIMENSIONS IN MILLIMETER	RS.			
2.	DIMENSIONING AND TOLERANCI	NG PER ASME Y14.5M	-1994.		
3.	MAXIMUM SOLDER BALL DIAME	TER MEASURED PARA	LLEL TO DA	TUM A.	
4.	- _ DATUM A, THE SEATING PLANI _ SOLDER BALLS.	E, IS DETERMINED BY	THE SPHER	RICAL CROWNS OF	THE
5.	A PARALLELISM MEASUREMENT S OF PACKAGE.	SHALL EXCLUDE ANY E	EFFECT OF I	MARK ON TOP SU	RFACE
©	NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	MECHANICAL OU			N NOT TO SCALE
		MECHANICAL 00			
TITLE:	MAPBGA,			T NO: 98ASA004	
	121 I/O, 8 X 8 X 1 0.65 MM PITC			D: JEDEC MO-27	
		1.1	SOT1533-	- 2	08 JAN 2016

SOT1533-2

LFBGA121, plastic, low profile fine-pitch ball grid array; 121 bumps; 0.65 mm pitch; 8 mm x 8 mm x 1.3 mm body

3 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

SOT1533-2

LFBGA121, plastic, low profile fine-pitch ball grid array; 121 bumps; 0.65 mm pitch; 8 mm x 8 mm x 1.3 mm body

Contents

1	Package summary	1
2	Package outline	2
3	Legal information	4

© NXP B.V. 2019.

All rights reserved.

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com Date of release: 25 January 2019